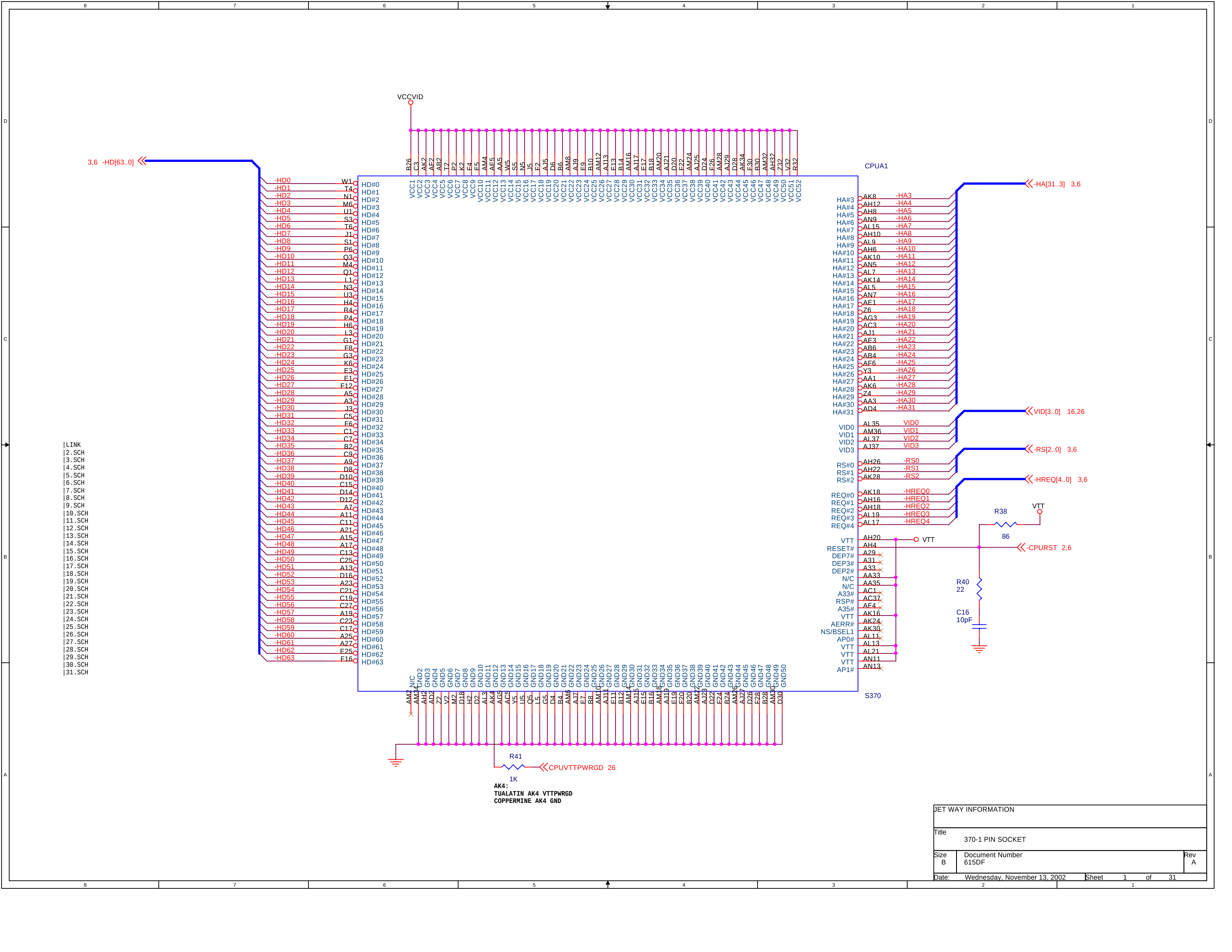


VCCVID
CPUA1
S370
3,6 -HD[63..0]
-HA[31..3] 3,6
VID[3..0] 16,26
-RS[2..0] 3,6
-HREQ[4..0] 3,6
R38
86
VTT
-CPURST 2,6
R40
22
C16
10pF
R41
1K
CPUVTTPWRGD 26
AK4:
TUALATIN AK4 VTTPWRGD
COPPERMINE AK4 GND
|LINK
|2.SCH
|3.SCH
|4.SCH
|5.SCH
|6.SCH
|7.SCH
|8.SCH
|9.SCH
|10.SCH
|11.SCH
|12.SCH
|13.SCH
|14.SCH
|15.SCH
|16.SCH
|17.SCH
|18.SCH
|19.SCH
|20.SCH
|21.SCH
|22.SCH
|23.SCH
|24.SCH
|25.SCH
|26.SCH
|27.SCH
|28.SCH
|29.SCH
|30.SCH
|31.SCH
VTT
RESET#
DEP7#
DEP3#
DEP2#
N/C
N/C
A33#
RSP#
A35#
VTT
AERR#
NS/BSEL1
AP0#
VTT
VTT
VTT
AP1#
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03/27/02 MODIFY FOR C3 OLD CPU

Q33 2N7002 — TUAL5, P3-X34, VTT

AK22:
TUALATIN AK22 CMOSREF
COPPERMINE AK22 GTLREF

	AJ33	AJ31
INTEL	BSEL0	BSEL1
JOSHUA	AJ33	AK30
66	0	0
100	1	0
133	1	1

JS3 :1-2:133 MHz
2-3:100 MHz

JS4: 1-2:100MHz
2-3:66MHz

CPUB1 — S370

X4:
TUALATIN X4 GND
COPPERMINE X4 RESET2#

DO NOT STUFF

Place site w/ in 0.5" of clock pin(W37)

AK36:
TUALATIN AK36 VID25
PPGA 370 AK36 VID4

AF36:
TUALATIN AF36 NC
COPPERMINE AF36 GND

370APICCLK:
TUALATIN CPU 0~2V
COPPERMINE CPU 0~2.5V

VCMOS Decoupling

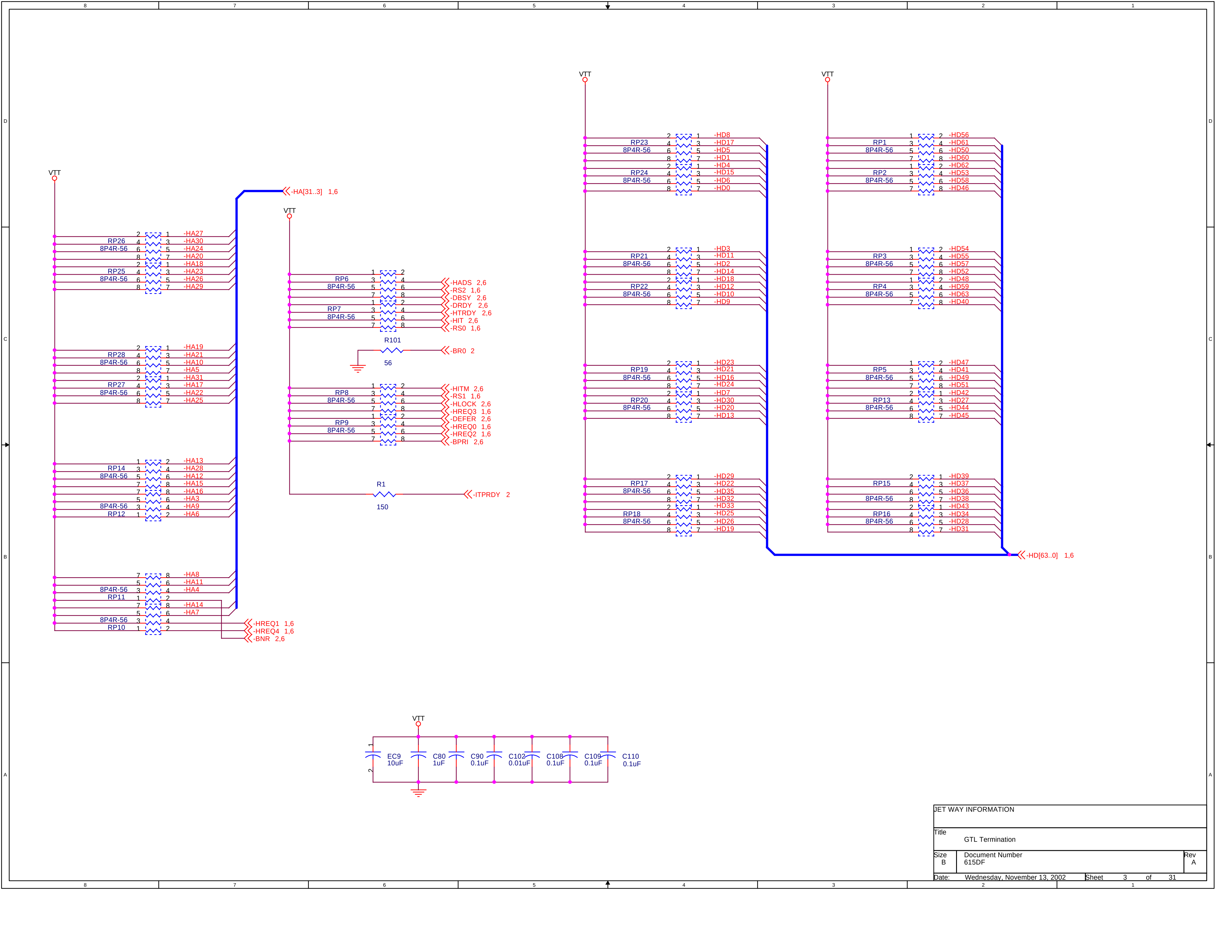
Place 0603 Package
Near VCMOS Processor Pin

	JS1	JS2	JS3	JS4
AUTO	ON	ON	OFF	OFF (66/100,100/100,133/100)
66/100	OFF	OFF	2-3	2-3
100/100	OFF	OFF	2-3	1-2
133/100	OFF	OFF	1-2	1-2
133/133	OFF	OFF	1-2	2-3

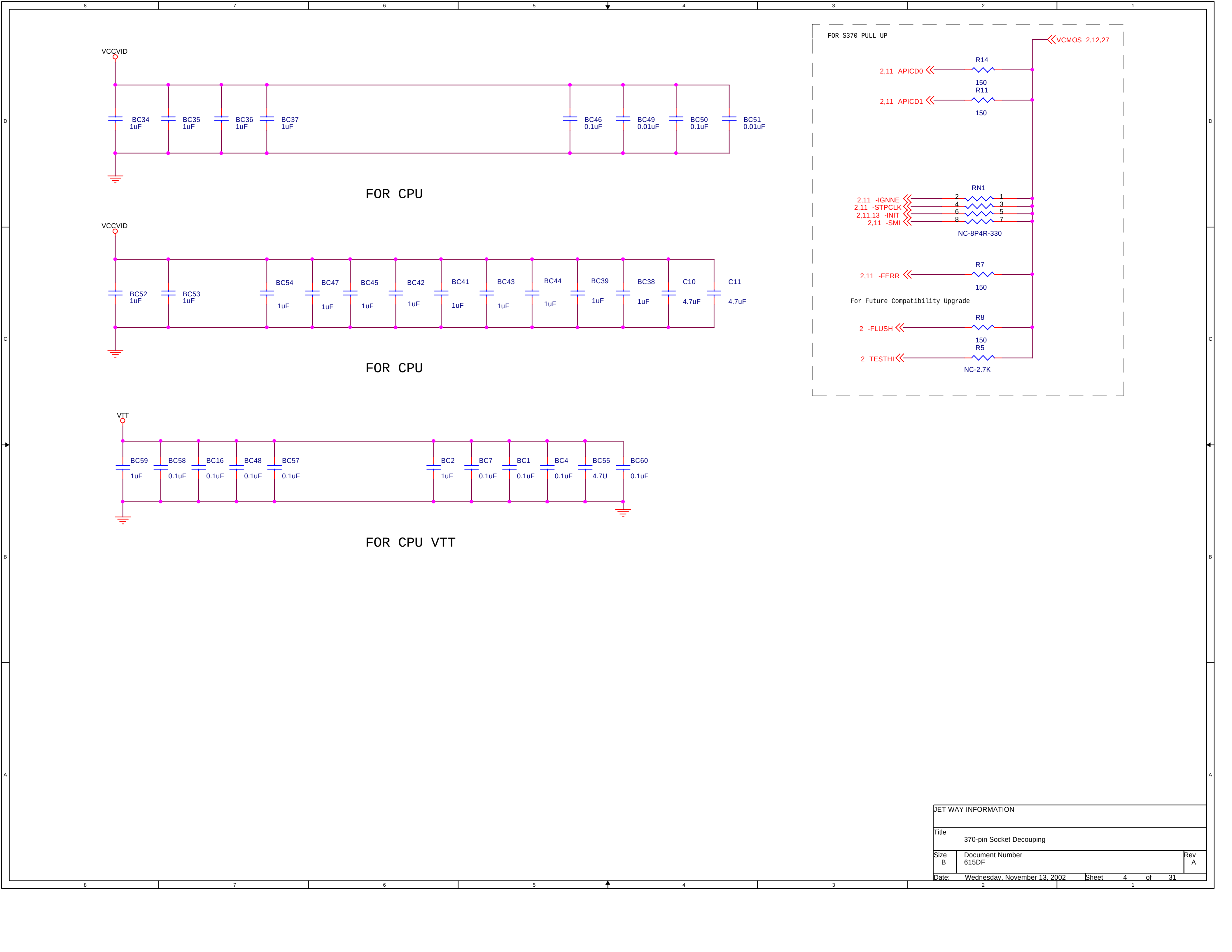
GTLREF Generation Circuit

Use 0603 Packages and distribute
within 500mils of Mendocino
GTLREF Inputs(1 cap for every 2 inputs).

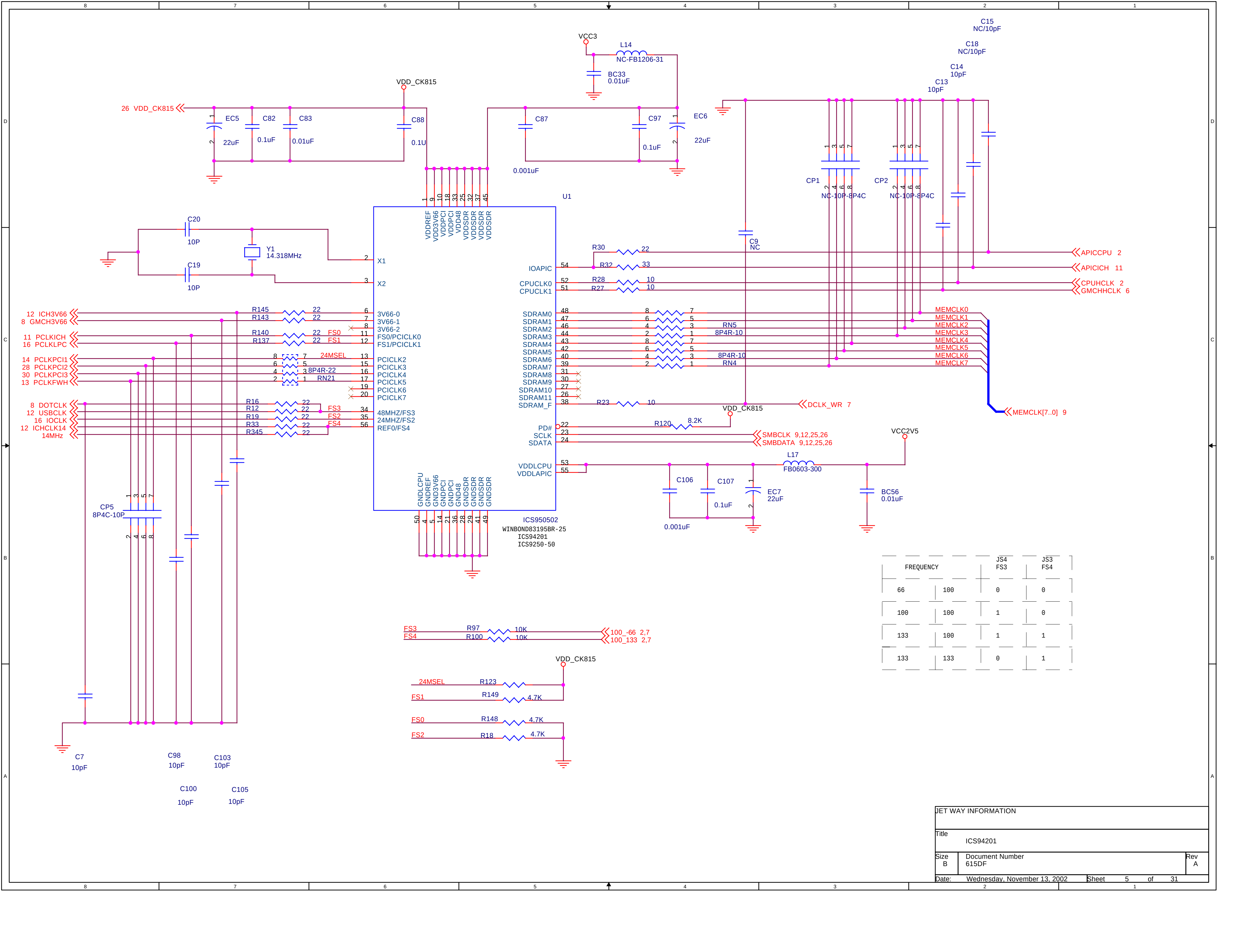
JET WAY INFORMATION		
Title	370-2 PIN SOCKET	
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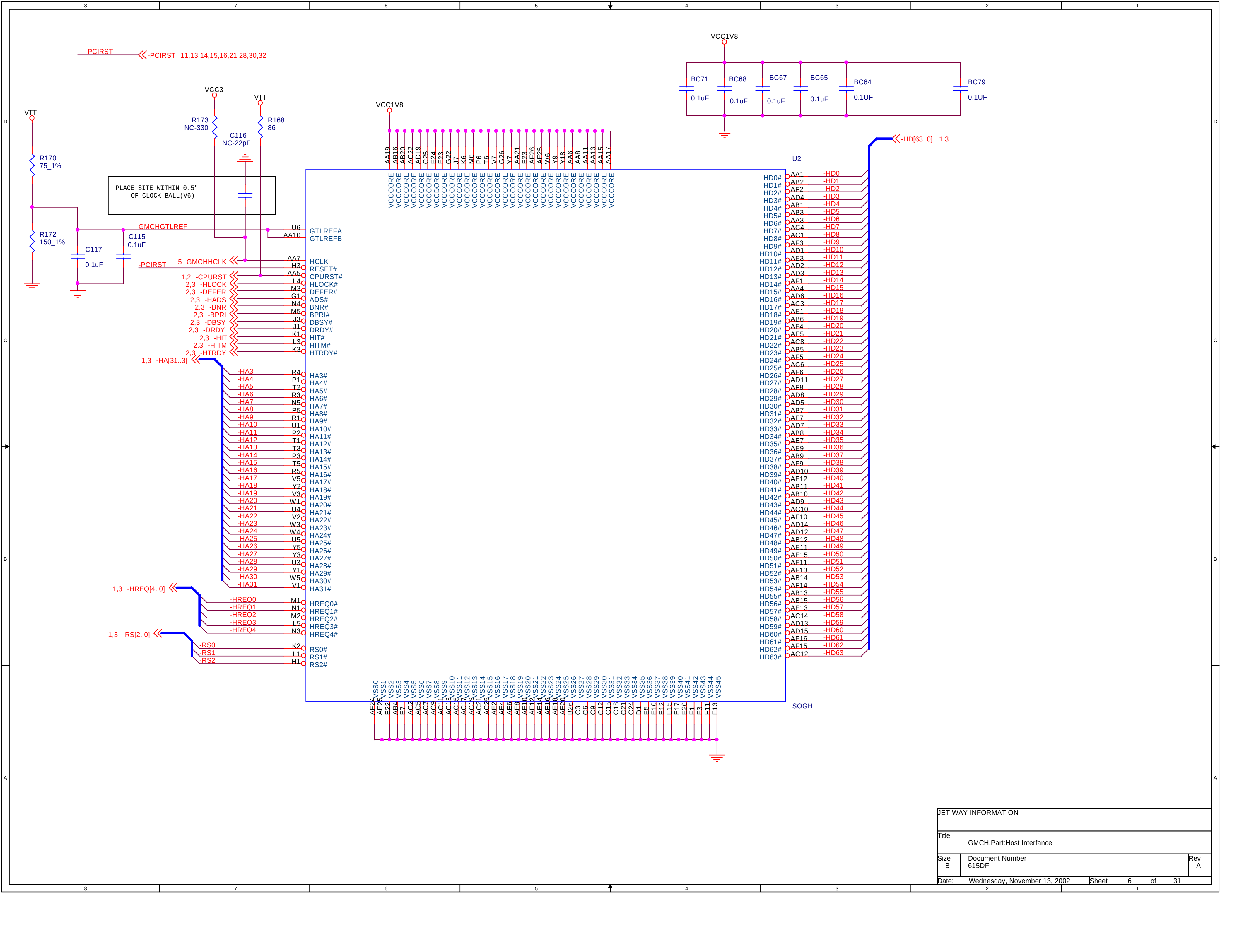
VTT
-HA[31..3] 1,6
RP26 8P4R-56
-HA27
-HA30
-HA24
-HA20
RP25 8P4R-56
-HA18
-HA23
-HA26
-HA29
RP28 8P4R-56
-HA19
-HA21
-HA10
-HA5
RP27 8P4R-56
-HA31
-HA17
-HA22
-HA25
RP14 8P4R-56
-HA13
-HA28
-HA12
-HA15
RP12 8P4R-56
-HA16
-HA3
-HA9
-HA6
RP11 8P4R-56
-HA8
-HA11
-HA4
RP10 8P4R-56
-HA14
-HA7
-HREQ1 1,6
-HREQ4 1,6
-BNR 2,6
RP6 8P4R-56
-HADS 2,6
-RS2 1,6
-DBSY 2,6
RP7 8P4R-56
-DRDY 2,6
-HTRDY 2,6
-HIT 2,6
-RS0 1,6
R101
56
-BR0 2
RP8 8P4R-56
-HITM 2,6
-RS1 1,6
-HLOCK 2,6
-HREQ3 1,6
RP9 8P4R-56
-DEFER 2,6
-HREQ0 1,6
-HREQ2 1,6
-BPRI 2,6
R1
150
-ITPRDY 2
RP23 8P4R-56
-HD8
-HD17
-HD5
-HD1
RP24 8P4R-56
-HD4
-HD15
-HD6
-HD0
RP21 8P4R-56
-HD3
-HD11
-HD2
-HD14
RP22 8P4R-56
-HD18
-HD12
-HD10
-HD9
RP19 8P4R-56
-HD23
-HD21
-HD16
-HD24
RP20 8P4R-56
-HD7
-HD30
-HD20
-HD13
RP17 8P4R-56
-HD29
-HD22
-HD35
-HD32
RP18 8P4R-56
-HD33
-HD25
-HD26
-HD19
RP1 8P4R-56
-HD56
-HD61
-HD50
-HD60
RP2 8P4R-56
-HD62
-HD53
-HD58
-HD46
RP3 8P4R-56
-HD54
-HD55
-HD57
-HD52
RP4 8P4R-56
-HD48
-HD59
-HD63
-HD40
RP5 8P4R-56
-HD47
-HD41
-HD49
-HD51
RP13 8P4R-56
-HD42
-HD27
-HD44
-HD45
RP15 8P4R-56
-HD39
-HD37
-HD36
-HD38
RP16 8P4R-56
-HD43
-HD34
-HD28
-HD31
-HD[63..0] 1,6
EC9 10uF
C80 1uF
C90 0.1uF
C102 0.01uF
C108 0.1uF
C109 0.1uF
C110 0.1uF
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VCCVID
BC34 1uF
BC35 1uF
BC36 1uF
BC37 1uF
BC46 0.1uF
BC49 0.01uF
BC50 0.1uF
BC51 0.01uF
FOR CPU
VCCVID
BC52 1uF
BC53 1uF
BC54 1uF
BC47 1uF
BC45 1uF
BC42 1uF
BC41 1uF
BC43 1uF
BC44 1uF
BC39 1uF
BC38 1uF
C10 4.7uF
C11 4.7uF
FOR CPU
VTT
BC59 1uF
BC58 0.1uF
BC16 0.1uF
BC48 0.1uF
BC57 0.1uF
BC2 1uF
BC7 0.1uF
BC1 0.1uF
BC4 0.1uF
BC55 4.7U
BC60 0.1uF
FOR CPU VTT
FOR S370 PULL UP
VCMOS 2,12,27
R14 150
2,11 APICD0
R11 150
2,11 APICD1
RN1
2,11 -IGNNE
2,11 -STPCLK
2,11,13 -INIT
2,11 -SMI
NC-8P4R-330
R7 150
2,11 -FERR
For Future Compatibility Upgrade
R8 150
2 -FLUSH
R5 NC-2.7K
2 TESTHI
JET WAY INFORMATION
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370-pin Socket Decouping
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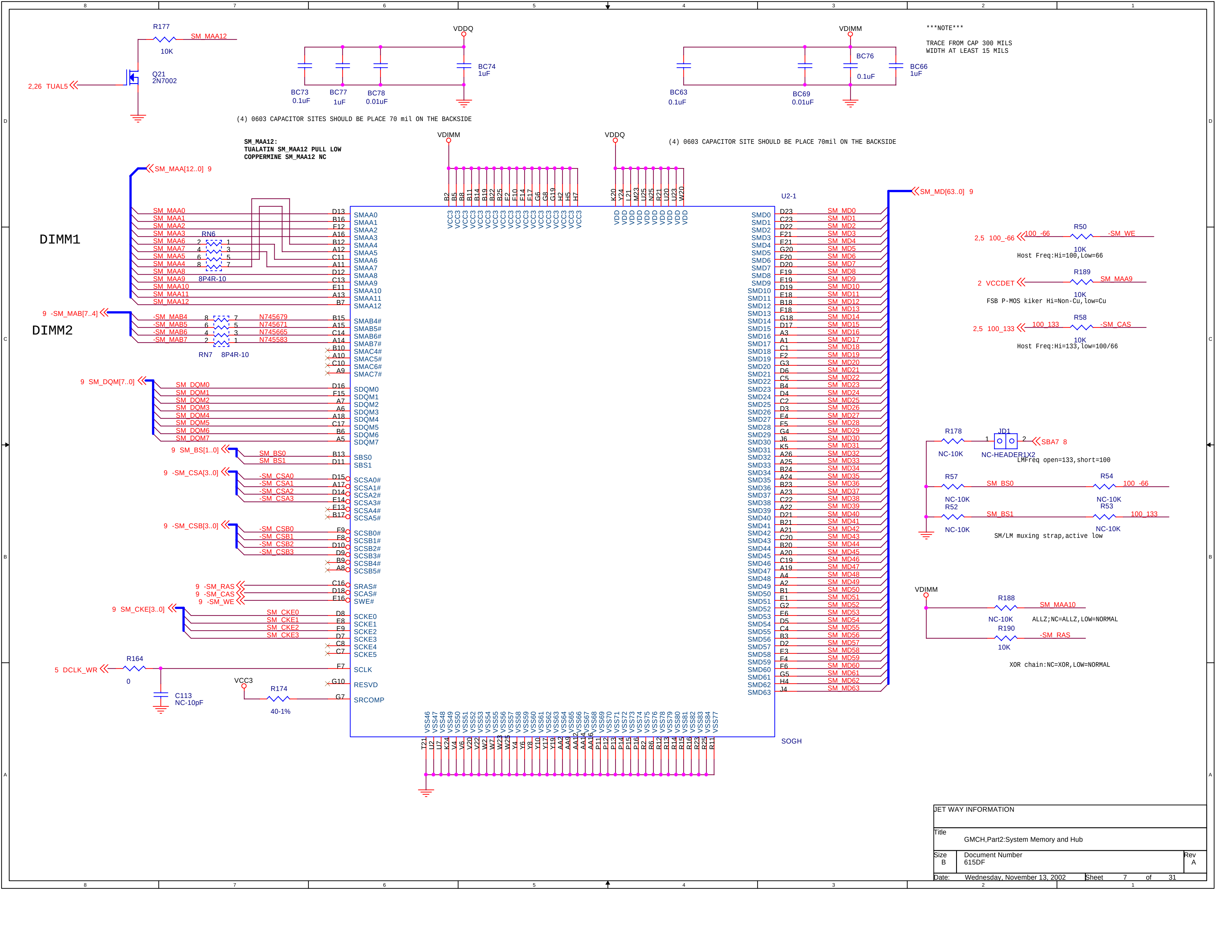


VCC3
L14
NC-FB1206-31
BC33
0.01uF
VDD_CK815
26 VDD_CK815
EC5
22uF
C82
0.1uF
C83
0.01uF
C88
0.1U
C87
0.001uF
C97
0.1uF
EC6
22uF
C15
NC/10pF
C18
NC/10pF
C14
10pF
C13
10pF
CP1
NC-10P-8P4C
CP2
NC-10P-8P4C
U1
C20
10P
Y1
14.318MHz
C19
10P
X1
X2
VDDREF VDD3V66 VDDPCI VDDPCI VDD48 VDDSDR VDDSDR VDDSDR VDDSDR
IOAPIC
CPUCLK0
CPUCLK1
R30 22
R32 33
R28 10
R27 10
C9
NC
APICCPU 2
APICICH 11
CPUHCLK 2
GMCHHCLK 6
12 ICH3V66
8 GMCH3V66
11 PCLKICH
16 PCLKLPC
14 PCLKPCI1
28 PCLKPCI2
30 PCLKPCI3
13 PCLKFWH
8 DOTCLK
12 USBCLK
16 IOCLK
12 ICHCLK14
14MHz
R145 22
R143 22
R140 22 FS0
R137 22 FS1
24MSEL
8P4R-22
RN21
R16 22
R12 22 FS3
R19 22 FS2
R33 22 FS4
R345 22
3V66-0
3V66-1
3V66-2
FS0/PCICLK0
FS1/PCICLK1
PCICLK2
PCICLK3
PCICLK4
PCICLK5
PCICLK6
PCICLK7
48MHZ/FS3
24MHZ/FS2
REF0/FS4
SDRAM0
SDRAM1
SDRAM2
SDRAM3
SDRAM4
SDRAM5
SDRAM6
SDRAM7
SDRAM8
SDRAM9
SDRAM10
SDRAM11
SDRAM_F
RN5
8P4R-10
8P4R-10
RN4
MEMCLK0
MEMCLK1
MEMCLK2
MEMCLK3
MEMCLK4
MEMCLK5
MEMCLK6
MEMCLK7
MEMCLK[7..0] 9
R23 10
DCLK_WR 7
VDD_CK815
PD#
SCLK
SDATA
R120 8.2K
SMBCLK 9,12,25,26
SMBDATA 9,12,25,26
VCC2V5
L17
FB0603-300
VDDLCPU
VDDLAPIC
C106
0.001uF
C107
0.1uF
EC7
22uF
BC56
0.01uF
GNDLCPU GNDREF GND3V66 GND3V66 GNDPCI GNDPCI GND48 GNDSDR GNDSDR GNDSDR GNDSDR
ICS950502
WINBOND83195BR-25
ICS94201
ICS9250-50
CP5
8P4C-10P
C7
10pF
C98
10pF
C103
10pF
C100
10pF
C105
10pF
FS3 R97 10K
FS4 R100 10K
100_-66 2,7
100_133 2,7
VDD_CK815
24MSEL R123
FS1 R149 4.7K
FS0 R148 4.7K
FS2 R18 4.7K
FREQUENCY
JS4 FS3
JS3 FS4
66 100 0 0
100 100 1 0
133 100 1 1
133 133 0 1
JET WAY INFORMATION
Title
ICS94201
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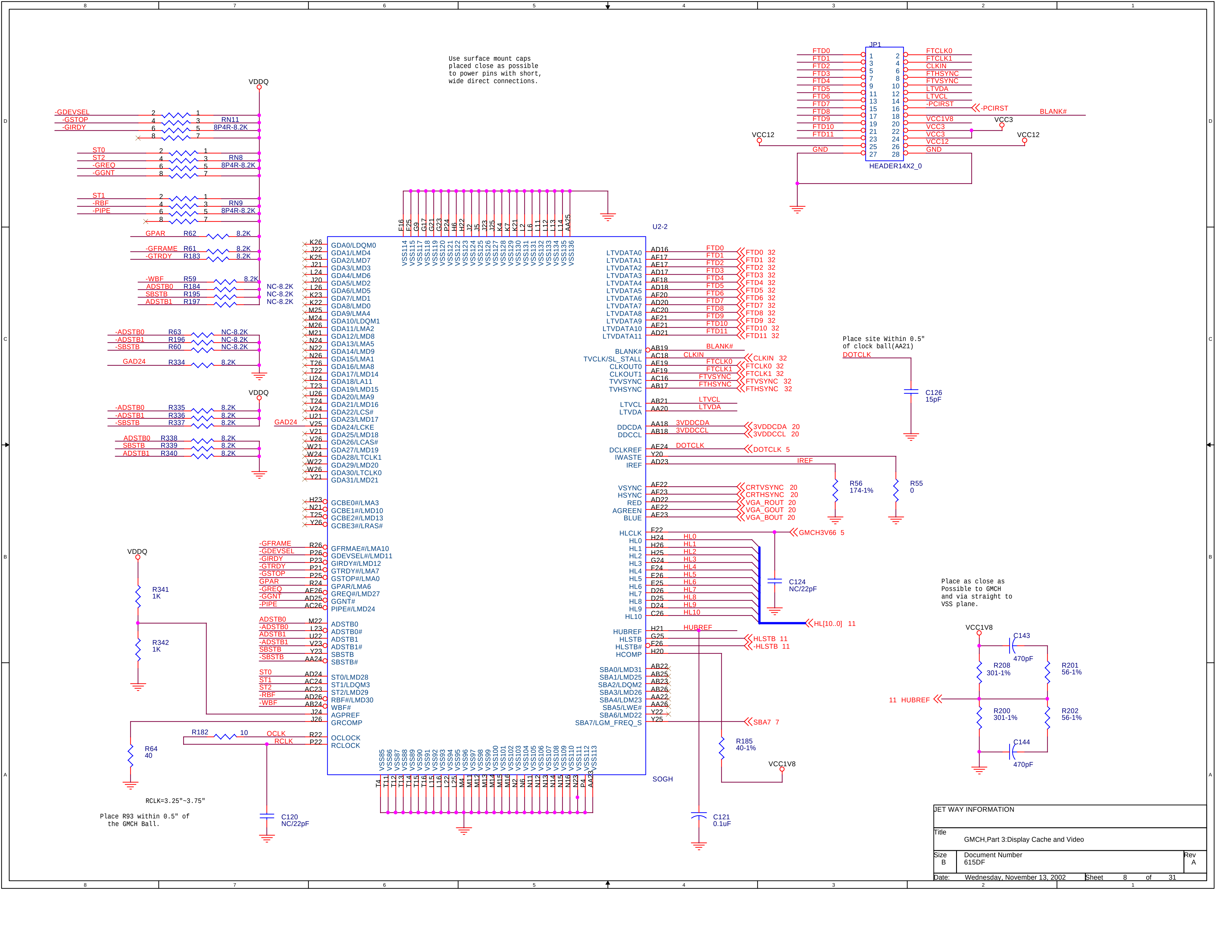


JET WAY INFORMATION

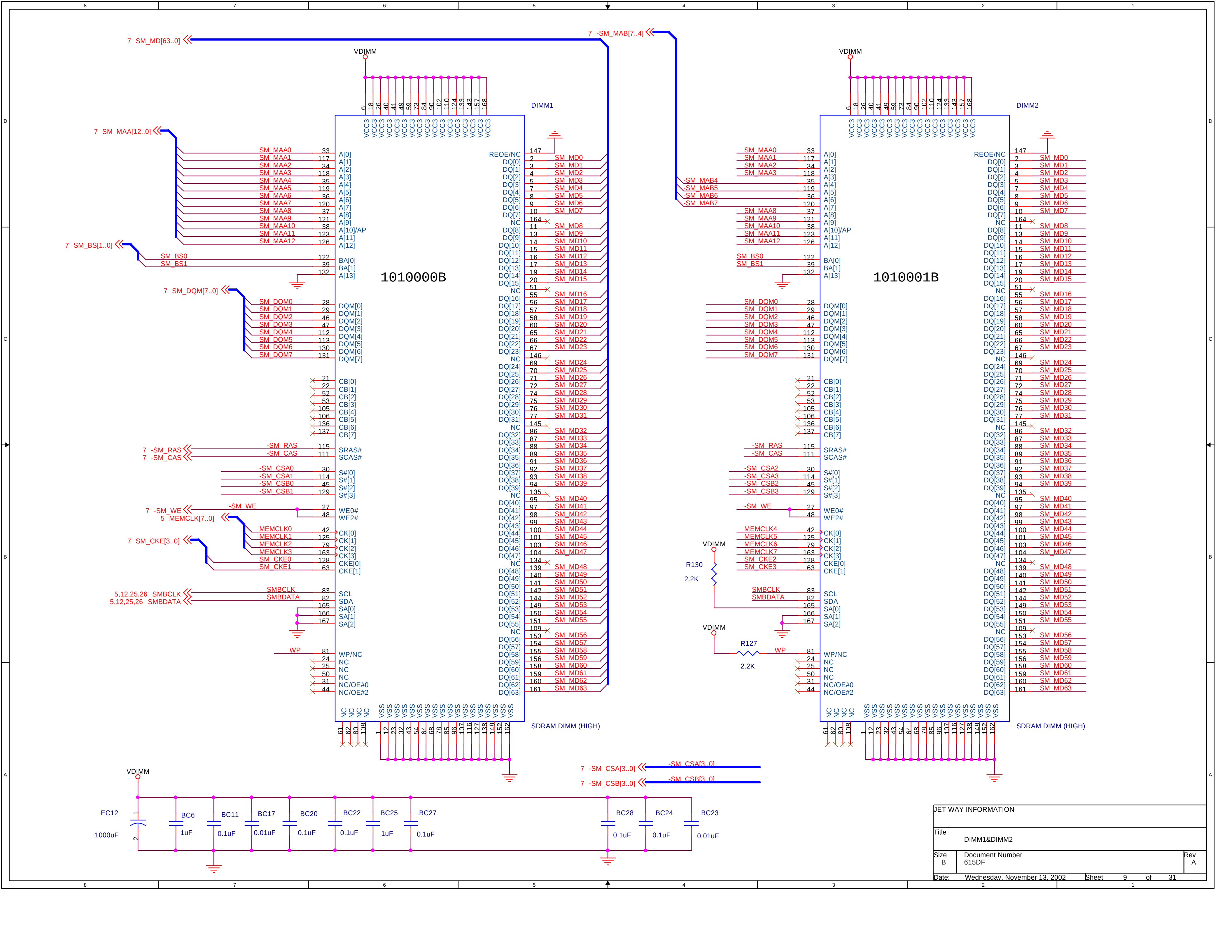
Title	GMCH,Part:Host Interfance	
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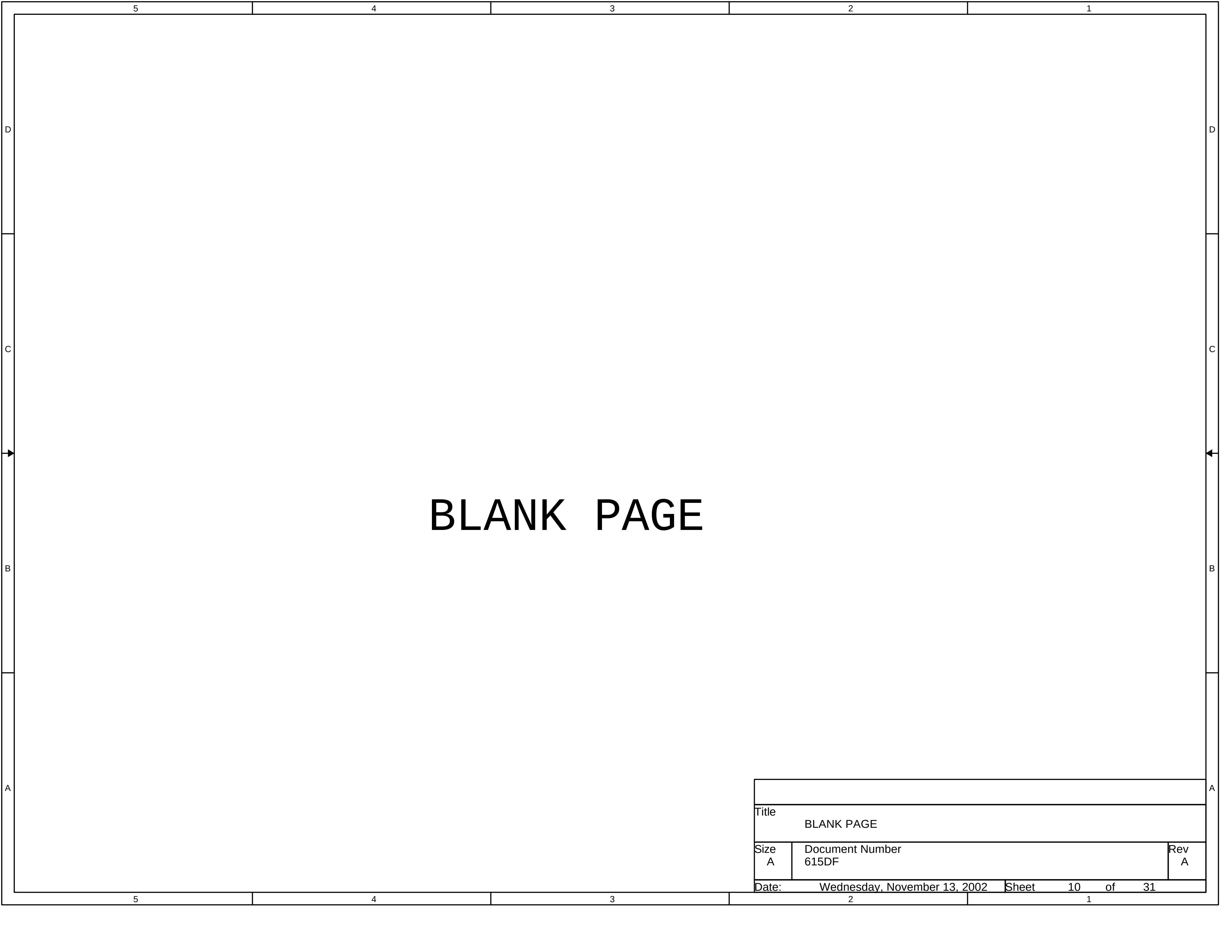
DIMM1
DIMM2
SM_MAA12:
TUALATIN SM_MAA12 PULL LOW
COPPERMINE SM_MAA12 NC
(4) 0603 CAPACITOR SITES SHOULD BE PLACE 70 mil ON THE BACKSIDE
(4) 0603 CAPACITOR SITE SHOULD BE PLACE 70mil ON THE BACKSIDE
NOTE
TRACE FROM CAP 300 MILS
WIDTH AT LEAST 15 MILS
U2-1
SOGH
Host Freq:Hi=100,Low=66
FSB P-MOS kiker Hi=Non-Cu,low=Cu
Host Freq:Hi=133,low=100/66
LMFreq open=133,short=100
SM/LM muxing strap,active low
ALLZ;NC=ALLZ,LOW=NORMAL
XOR chain:NC=XOR,LOW=NORMAL
JET WAY INFORMATION
Title
GMCH,Part2:System Memory and Hub
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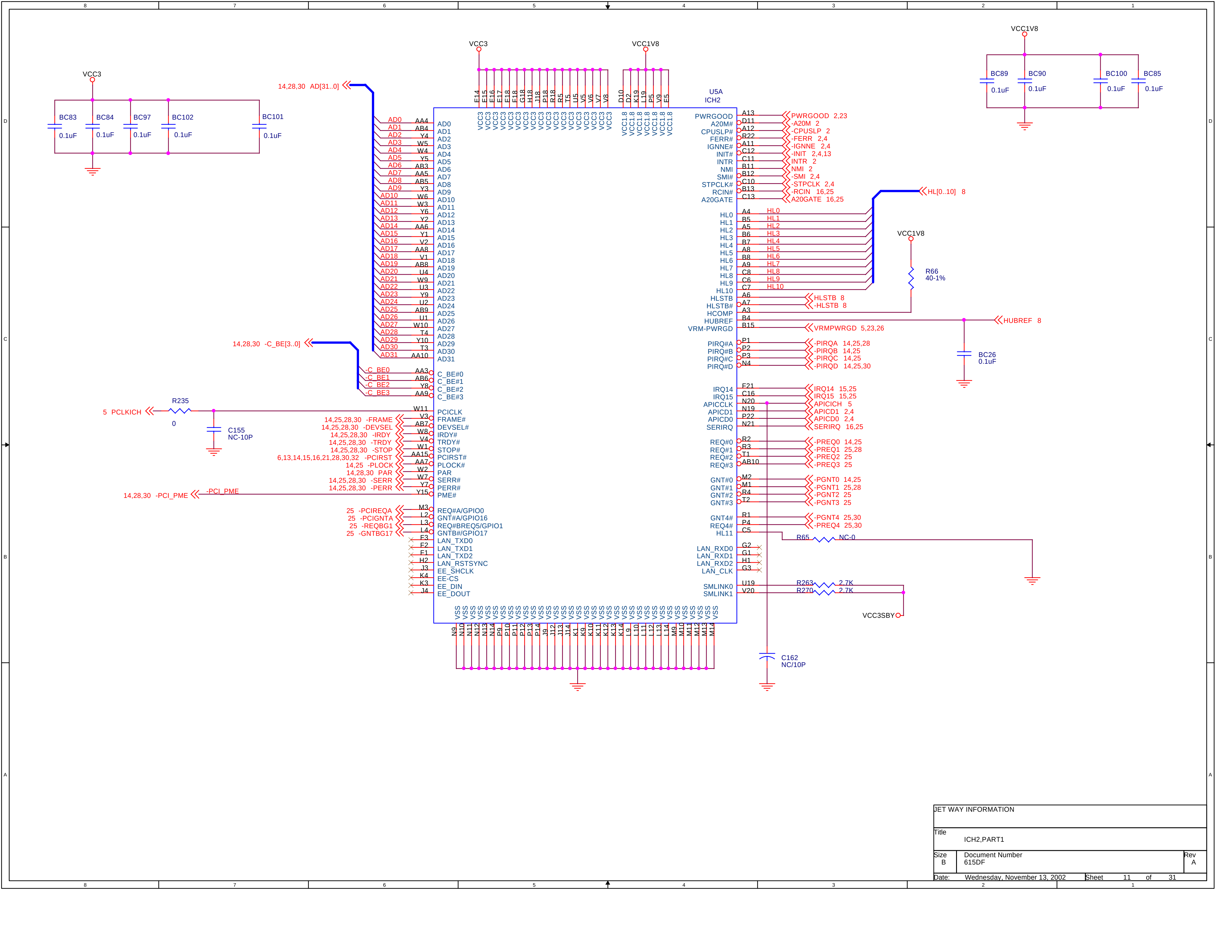


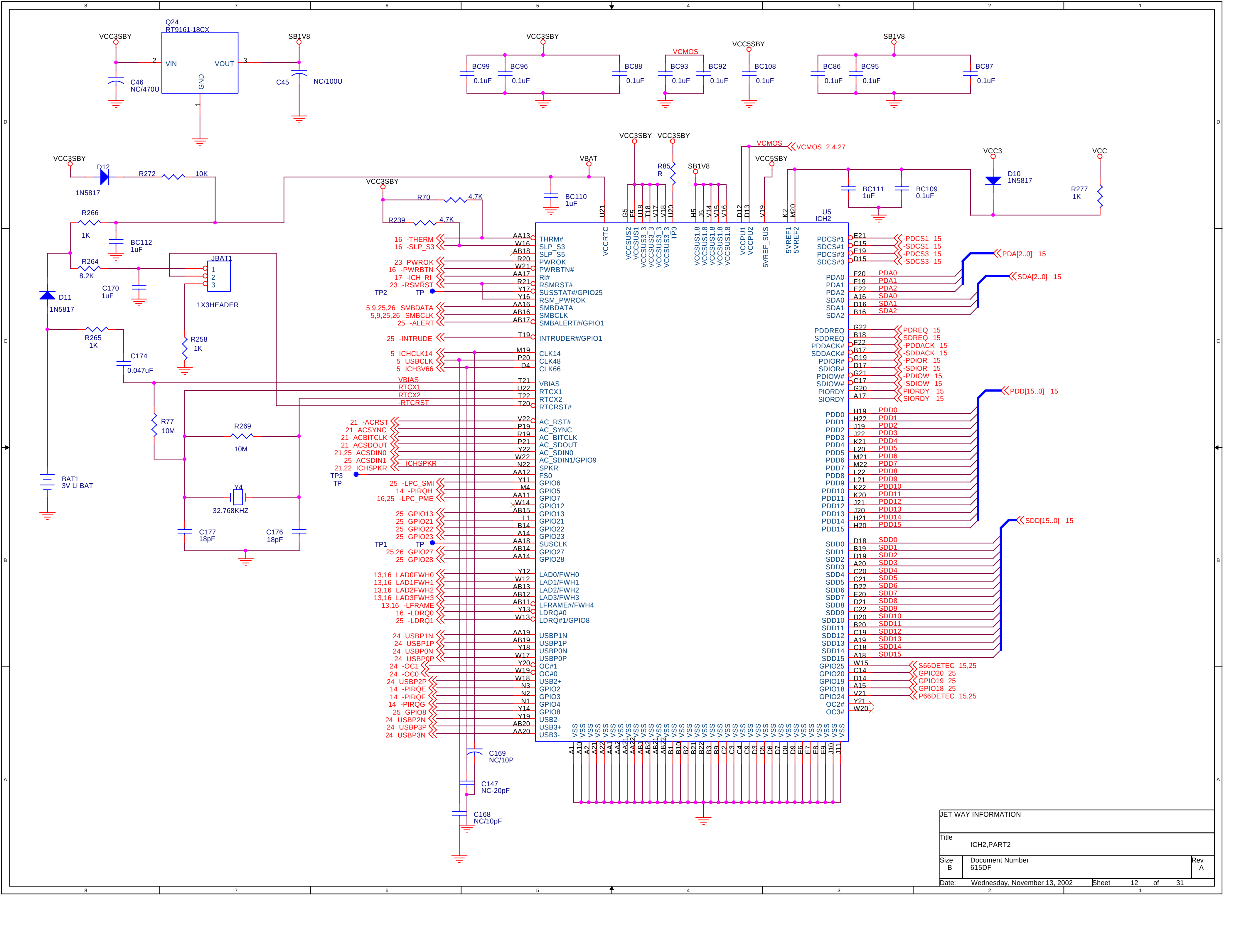
Use surface mount caps placed close as possible to power pins with short, wide direct connections.
JP1
HEADER14X2_0
U2-2
SOGH
Place site Within 0.5" of clock ball(AA21)
DOTCLK
C126
15pF
R56
174-1%
R55
0
C124
NC/22pF
Place as close as Possible to GMCH and via straight to VSS plane.
VCC1V8
C143
470pF
R208
301-1%
R201
56-1%
11 HUBREF
R200
301-1%
R202
56-1%
C144
470pF
R185
40-1%
C121
0.1uF
C120
NC/22pF
R64
40
R341
1K
R342
1K
RCLK=3.25"~3.75"
Place R93 within 0.5" of the GMCH Ball.
RN11
8P4R-8.2K
RN8
8P4R-8.2K
RN9
8P4R-8.2K
VDDQ
VCC12
VCC3
BLANK#
-PCIRST
GMCH3V66 5
HL[10..0] 11
HLSTB 11
-HLSTB 11
SBA7 7
DOTCLK 5
3VDDCDA 20
3VDDCCL 20
CRTVSYNC 20
CRTHSYNC 20
VGA_ROUT 20
VGA_GOUT 20
VGA_BOUT 20
JET WAY INFORMATION
Title
GMCH,Part 3:Display Cache and Video
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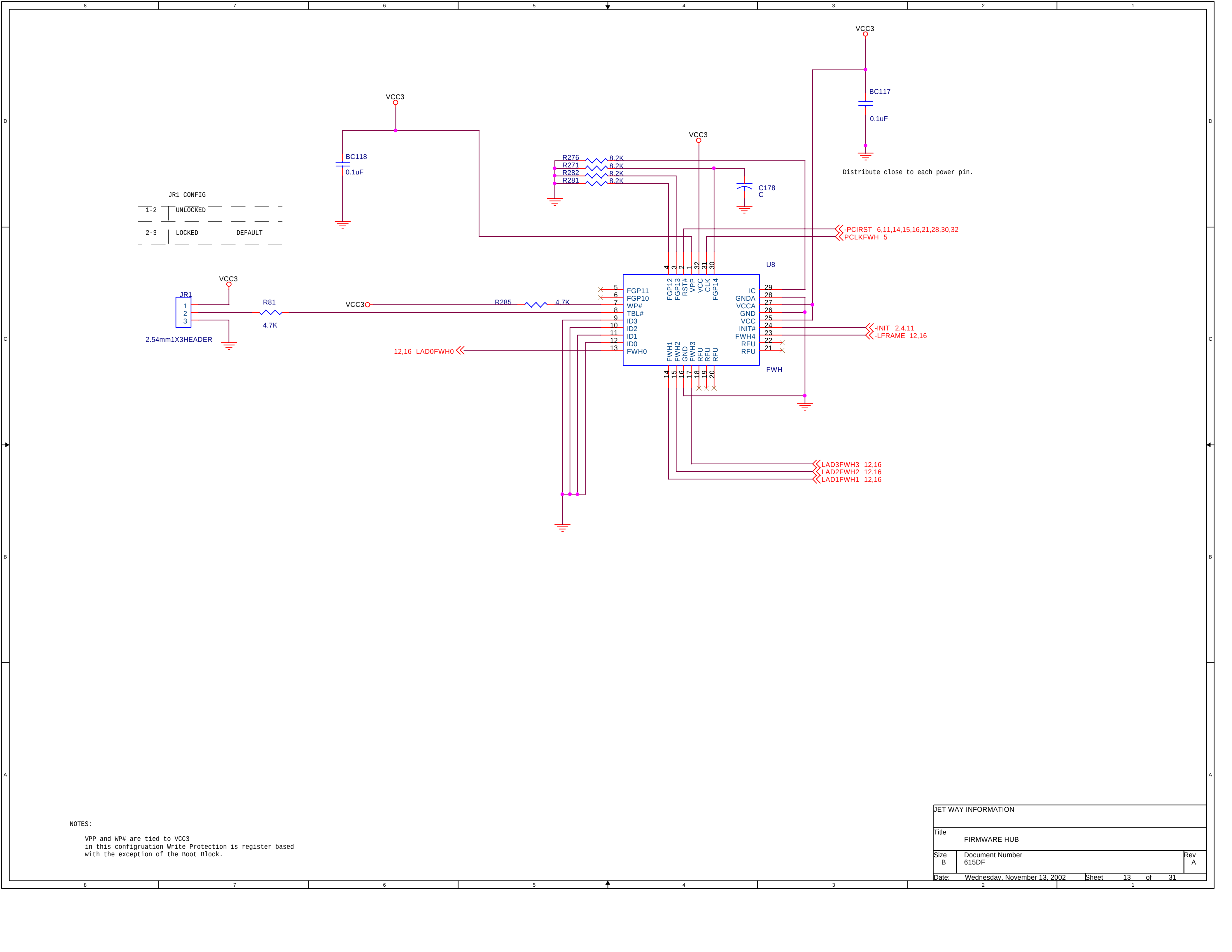


7 SM_MD[63..0]
7 -SM_MAB[7..4]
VDIMM
DIMM1
DIMM2
7 SM_MAA[12..0]
7 SM_BS[1..0]
7 SM_DQM[7..0]
1010000B
1010001B
SM_MAA0 SM_MAA1 SM_MAA2 SM_MAA3 SM_MAA4 SM_MAA5 SM_MAA6 SM_MAA7 SM_MAA8 SM_MAA9 SM_MAA10 SM_MAA11 SM_MAA12
-SM_MAB4 -SM_MAB5 -SM_MAB6 -SM_MAB7
SM_BS0 SM_BS1
SM_DQM0 SM_DQM1 SM_DQM2 SM_DQM3 SM_DQM4 SM_DQM5 SM_DQM6 SM_DQM7
A[0] A[1] A[2] A[3] A[4] A[5] A[6] A[7] A[8] A[9] A[10]/AP A[11] A[12] BA[0] BA[1] A[13]
DQM[0] DQM[1] DQM[2] DQM[3] DQM[4] DQM[5] DQM[6] DQM[7]
CB[0] CB[1] CB[2] CB[3] CB[4] CB[5] CB[6] CB[7]
REOE/NC
DQ[0] … DQ[63]
SM_MD0 … SM_MD63
7 -SM_RAS
7 -SM_CAS
-SM_RAS -SM_CAS
SRAS# SCAS#
-SM_CSA0 -SM_CSA1 -SM_CSB0 -SM_CSB1
-SM_CSA2 -SM_CSA3 -SM_CSB2 -SM_CSB3
S#[0] S#[1] S#[2] S#[3]
7 -SM_WE
-SM_WE
WE0# WE2#
5 MEMCLK[7..0]
MEMCLK0 MEMCLK1 MEMCLK2 MEMCLK3
MEMCLK4 MEMCLK5 MEMCLK6 MEMCLK7
7 SM_CKE[3..0]
SM_CKE0 SM_CKE1 SM_CKE2 SM_CKE3
CK[0] CK[1] CK[2] CK[3] CKE[0] CKE[1]
5,12,25,26 SMBCLK
5,12,25,26 SMBDATA
SMBCLK SMBDATA
SCL SDA SA[0] SA[1] SA[2]
WP
WP/NC NC NC NC NC/OE#0 NC/OE#2
R130 2.2K
R127 2.2K
VCC3
VSS
SDRAM DIMM (HIGH)
7 -SM_CSA[3..0]
7 -SM_CSB[3..0]
-SM_CSA[3..0]
-SM_CSB[3..0]
EC12 1000uF
BC6 1uF
BC11 0.1uF
BC17 0.01uF
BC20 0.1uF
BC22 0.1uF
BC25 1uF
BC27 0.1uF
BC28 0.1uF
BC24 0.1uF
BC23 0.01uF
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DIMM1&DIMM2
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4
3
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1
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VCC3
BC83 0.1uF
BC84 0.1uF
BC97 0.1uF
BC102 0.1uF
BC101 0.1uF
14,28,30 AD[31..0]
VCC3
VCC1V8
U5A
ICH2
F14 F15 F16 F17 F18 F18 G18 H18 J18 P18 R18 R5 T5 U5 V5 V6 V7 V8
D10 D2 K19 L19 P5 V9 E5
VCC1V8
BC89 0.1uF
BC90 0.1uF
BC100 0.1uF
BC85 0.1uF
AD0 AA4 AD0
AD1 AB4 AD1
AD2 Y4 AD2
AD3 W5 AD3
AD4 W4 AD4
AD5 Y5 AD5
AD6 AB3 AD6
AD7 AA5 AD7
AD8 AB5 AD8
AD9 Y3 AD9
AD10 W6 AD10
AD11 W3 AD11
AD12 Y6 AD12
AD13 Y2 AD13
AD14 AA6 AD14
AD15 Y1 AD15
AD16 V2 AD16
AD17 AA8 AD17
AD18 V1 AD18
AD19 AB8 AD19
AD20 U4 AD20
AD21 W9 AD21
AD22 U3 AD22
AD23 Y9 AD23
AD24 U2 AD24
AD25 AB9 AD25
AD26 U1 AD26
AD27 W10 AD27
AD28 T4 AD28
AD29 Y10 AD29
AD30 T3 AD30
AD31 AA10 AD31
14,28,30 -C_BE[3..0]
-C_BE0 AA3 C_BE#0
-C_BE1 AB6 C_BE#1
-C_BE2 Y8 C_BE#2
-C_BE3 AA9 C_BE#3
5 PCLKICH
R235 0
W11 PCLKCLK
C155 NC-10P
14,25,28,30 -FRAME V3 FRAME#
14,25,28,30 -DEVSEL AB7 DEVSEL#
14,25,28,30 -IRDY W8 IRDY#
14,25,28,30 -TRDY V4 TRDY#
14,25,28,30 -STOP W1 STOP#
6,13,14,15,16,21,28,30,32 -PCIRST AA15 PCIRST#
14,25 -PLOCK AA7 PLOCK#
14,28,30 PAR W2 PAR
14,25,28,30 -SERR W7 SERR#
14,25,28,30 -PERR Y7 PERR#
14,28,30 -PCI_PME -PCI_PME Y15 PME#
25 -PCIREQA M3 REQ#A/GPIO0
25 -PCIGNTA L2 GNT#A/GPIO16
25 -REQBG1 L3 REQ#BREQ5/GPIO1
25 -GNTBG17 L4 GNTB#/GPIO17
F3 LAN_TXD0
F2 LAN_TXD1
F1 LAN_TXD2
H2 LAN_RSTSYNC
J3 EE_SHCLK
K4 EE_CS
K3 EE_DIN
J4 EE_DOUT
PWRGOOD A13 PWRGOOD 2,23
A20M# D11 -A20M 2
CPUSLP# A12 -CPUSLP 2
FERR# R22 -FERR 2,4
IGNNE# A11 -IGNNE 2,4
INIT# C12 -INIT 2,4,13
INTR C11 INTR 2
NMI B11 NMI 2
SMI# B12 -SMI 2,4
STPCLK# C10 -STPCLK 2,4
RCIN# B13 -RCIN 16,25
A20GATE C13 A20GATE 16,25
HL[0..10] 8
HL0 A4 HL0
HL1 B5 HL1
HL2 A5 HL2
HL3 B6 HL3
HL4 A8 HL4
HL5 B8 HL5
HL6 A9 HL6
HL7 C8 HL7
HL8 C6 HL8
HL9 C7 HL9
HL10 HL10
HLSTB A6 HLSTB 8
HLSTB# A7 -HLSTB 8
HCOMP A3
HUBREF B4 HUBREF 8
VRM-PWRGD B15 VRMPWRGD 5,23,26
VCC1V8
R66 40-1%
BC26 0.1uF
PIRQ#A P1 -PIRQA 14,25,28
PIRQ#B P2 -PIRQB 14,25
PIRQ#C P3 -PIRQC 14,25
PIRQ#D N4 -PIRQD 14,25,30
IRQ14 F21 IRQ14 15,25
IRQ15 C16 IRQ15 15,25
APICCLK N20 APICICH 5
APICD1 N19 APICD1 2,4
APICD0 P22 APICD0 2,4
SERIRQ N21 SERIRQ 16,25
REQ#0 R2 -PREQ0 14,25
REQ#1 R3 -PREQ1 25,28
REQ#2 T1 -PREQ2 25
REQ#3 AB10 -PREQ3 25
GNT#0 M2 -PGNT0 14,25
GNT#1 M1 -PGNT1 25,28
GNT#2 R4 -PGNT2 25
GNT#3 T2 -PGNT3 25
GNT4# R1 -PGNT4 25,30
REQ4# P4 -PREQ4 25,30
HL11 C5
R65 NC-0
LAN_RXD0 G2
LAN_RXD1 G1
LAN_RXD2 H1
LAN_CLK G3
SMLINK0 U19 R263 2.7K
SMLINK1 V20 R270 2.7K
VCC3SBY
C162 NC/10P
N9 N10 N11 N12 N13 N14 P9 P10 P11 P12 P13 P14 J9 J12 J13 J14 K1 K9 K10 K11 K12 K13 K14 L9 L10 L11 L12 L13 L14 M9 M10 M11 M12 M13 M14
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ICH2,PART1
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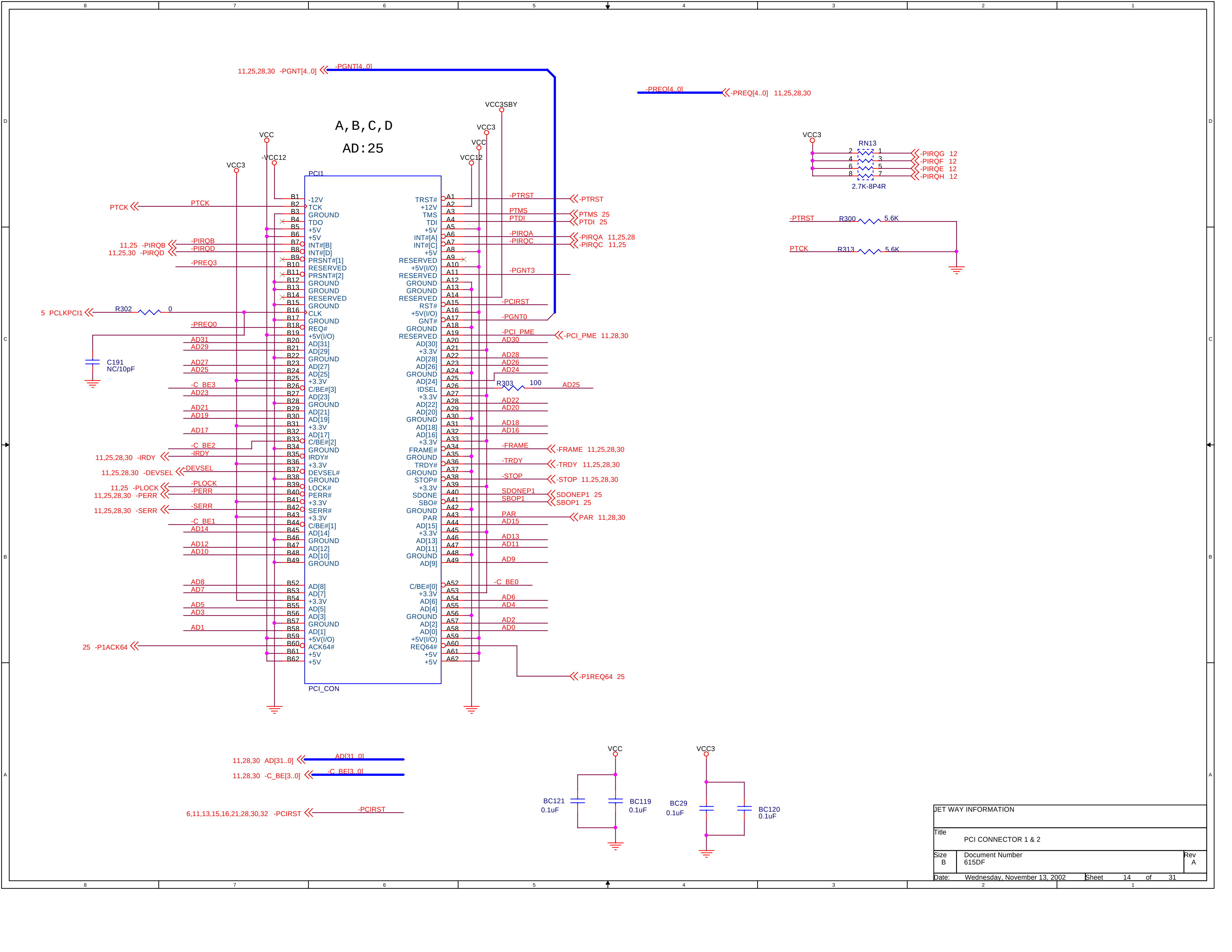

Q24
RT9161-18CX
VIN
VOUT
GND
VCC3SBY
SB1V8
C46
NC/470U
C45
NC/100U
VCC3SBY
BC99
BC96
BC88
0.1uF
VCMOS
BC93
BC92
VCC5SBY
BC108
SB1V8
BC86
BC95
BC87
VCC3SBY
D12
1N5817
R272
10K
VBAT
BC110
1uF
R70
4.7K
R239
4.7K
R266
1K
BC112
1uF
JBAT1
1X3HEADER
R264
8.2K
C170
1uF
D11
1N5817
R265
1K
R258
1K
C174
0.047uF
R77
10M
R269
10M
Y4
32.768KHZ
C177
18pF
C176
18pF
BAT1
3V Li BAT
R85
R
VCMOS 2,4,27
VCC5SBY
BC111
1uF
BC109
0.1uF
VCC3
D10
1N5817
VCC
R277
1K
U5
ICH2
16 -THERM
16 -SLP_S3
23 PWROK
16 -PWRBTN
17 -ICH_RI
23 -RSMRST
TP2
5,9,25,26 SMBDATA
5,9,25,26 SMBCLK
25 -ALERT
25 -INTRUDE
5 ICHCLK14
5 USBCLK
5 ICH3V66
VBIAS
RTCX1
RTCX2
-RTCRST
21 -ACRST
21 ACSYNC
21 ACBITCLK
21 ACSDOUT
21,25 ACSDIN0
25 ACSDIN1
21,22 ICHSPKR
TP3
25 -LPC_SMI
14 -PIRQH
16,25 -LPC_PME
25 GPIO13
25 GPIO21
25 GPIO22
25 GPIO23
TP1
25,26 GPIO27
25 GPIO28
13,16 LAD0FWH0
13,16 LAD1FWH1
13,16 LAD2FWH2
13,16 LAD3FWH3
13,16 -LFRAME
16 -LDRQ0
25 -LDRQ1
24 USBP1N
24 USBP1P
24 USBP0N
24 USBP0P
24 -OC1
24 -OC0
24 USBP2P
14 -PIRQE
14 -PIRQF
14 -PIRQG
25 GPIO8
24 USBP2N
24 USBP3P
24 USBP3N
C169
NC/10P
C147
NC-20pF
C168
NC/10pF
THRM#
SLP_S3
SLP_S5
PWROK
PWRBTN#
RI#
RSMRST#
SUSSTAT#/GPIO25
RSM_PWROK
SMBDATA
SMBCLK
SMBALERT#/GPIO1
INTRUDER#/GPIO1
CLK14
CLK48
CLK66
VBIAS
RTCX1
RTCX2
RTCRST#
AC_RST#
AC_SYNC
AC_BITCLK
AC_SDOUT
AC_SDIN0
AC_SDIN1/GPIO9
SPKR
FS0
GPIO6
GPIO5
GPIO7
GPIO12
GPIO13
GPIO21
GPIO22
GPIO23
SUSCLK
GPIO27
GPIO28
LAD0/FWH0
LAD1/FWH1
LAD2/FWH2
LAD3/FWH3
LFRAME#/FWH4
LDRQ#0
LDRQ#1/GPIO8
USBP1N
USBP1P
USBP0N
USBP0P
OC#1
OC#0
USB2+
GPIO2
GPIO3
GPIO4
GPIO8
USB2-
USB3+
USB3-
VCCRTC
VCCSUS2
VCCSUS1
VCCSUS3_3
VCCSUS1.8
VCCPU1
VCCPU2
5VREF_SUS
5VREF1
5VREF2
PDCS#1
SDCS#1
PDCS#3
SDCS#3
-PDCS1 15
-SDCS1 15
-PDCS3 15
-SDCS3 15
PDA[2..0] 15
SDA[2..0] 15
PDA0
PDA1
PDA2
SDA0
SDA1
SDA2
PDDREQ
SDDREQ
PDDACK#
SDDACK#
PDIOR#
SDIOR#
PDIOW#
SDIOW#
PIORDY
SIORDY
PDREQ 15
SDREQ 15
-PDDACK 15
-SDDACK 15
-PDIOR 15
-SDIOR 15
-PDIOW 15
-SDIOW 15
PIORDY 15
SIORDY 15
PDD[15..0] 15
PDD0
PDD1
PDD2
PDD3
PDD4
PDD5
PDD6
PDD7
PDD8
PDD9
PDD10
PDD11
PDD12
PDD13
PDD14
PDD15
SDD[15..0] 15
SDD0
SDD1
SDD2
SDD3
SDD4
SDD5
SDD6
SDD7
SDD8
SDD9
SDD10
SDD11
SDD12
SDD13
SDD14
SDD15
GPIO25
GPIO20
GPIO19
GPIO18
GPIO24
OC2#
OC3#
S66DETEC 15,25
GPIO20 25
GPIO19 25
GPIO18 25
P66DETEC 15,25
VSS
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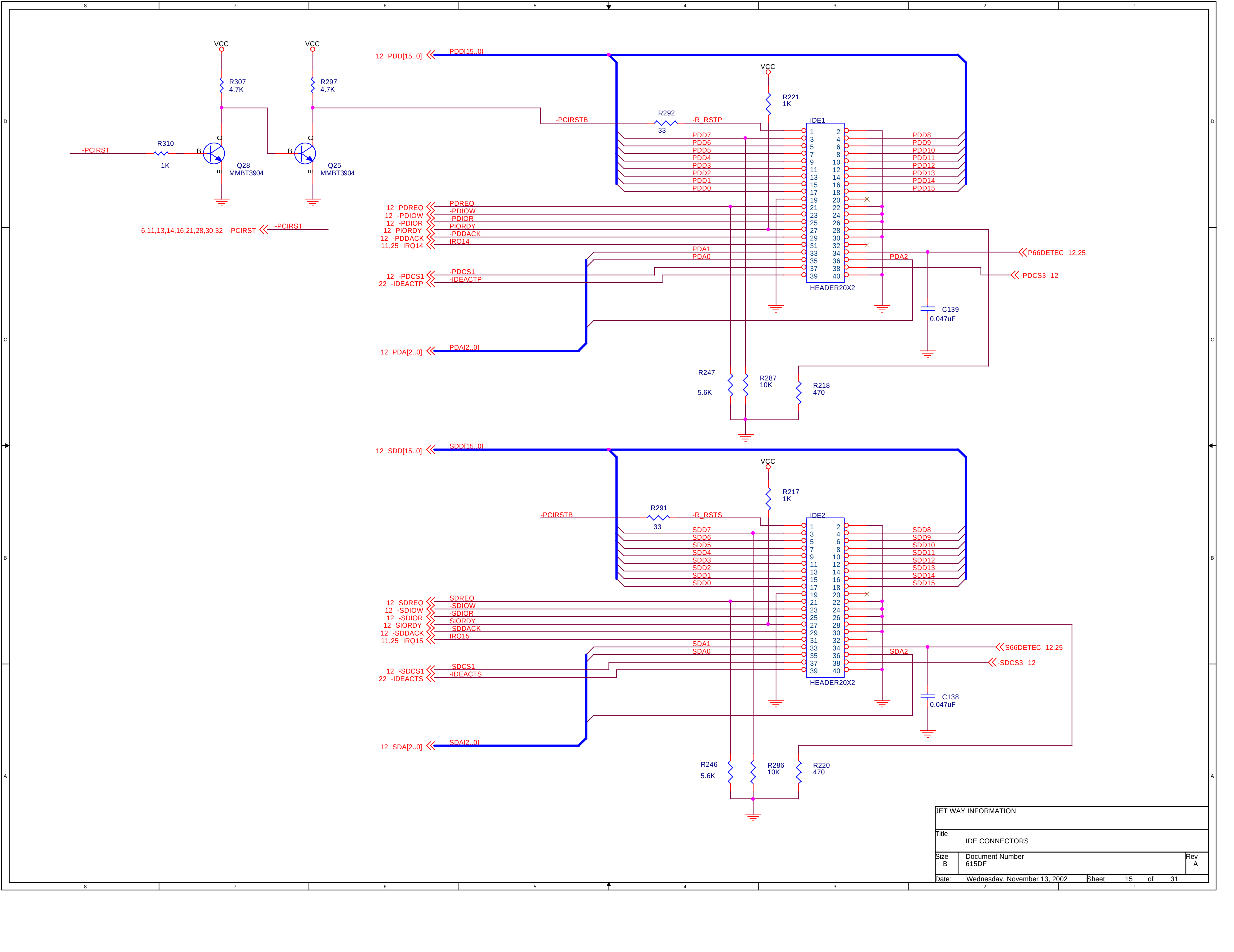
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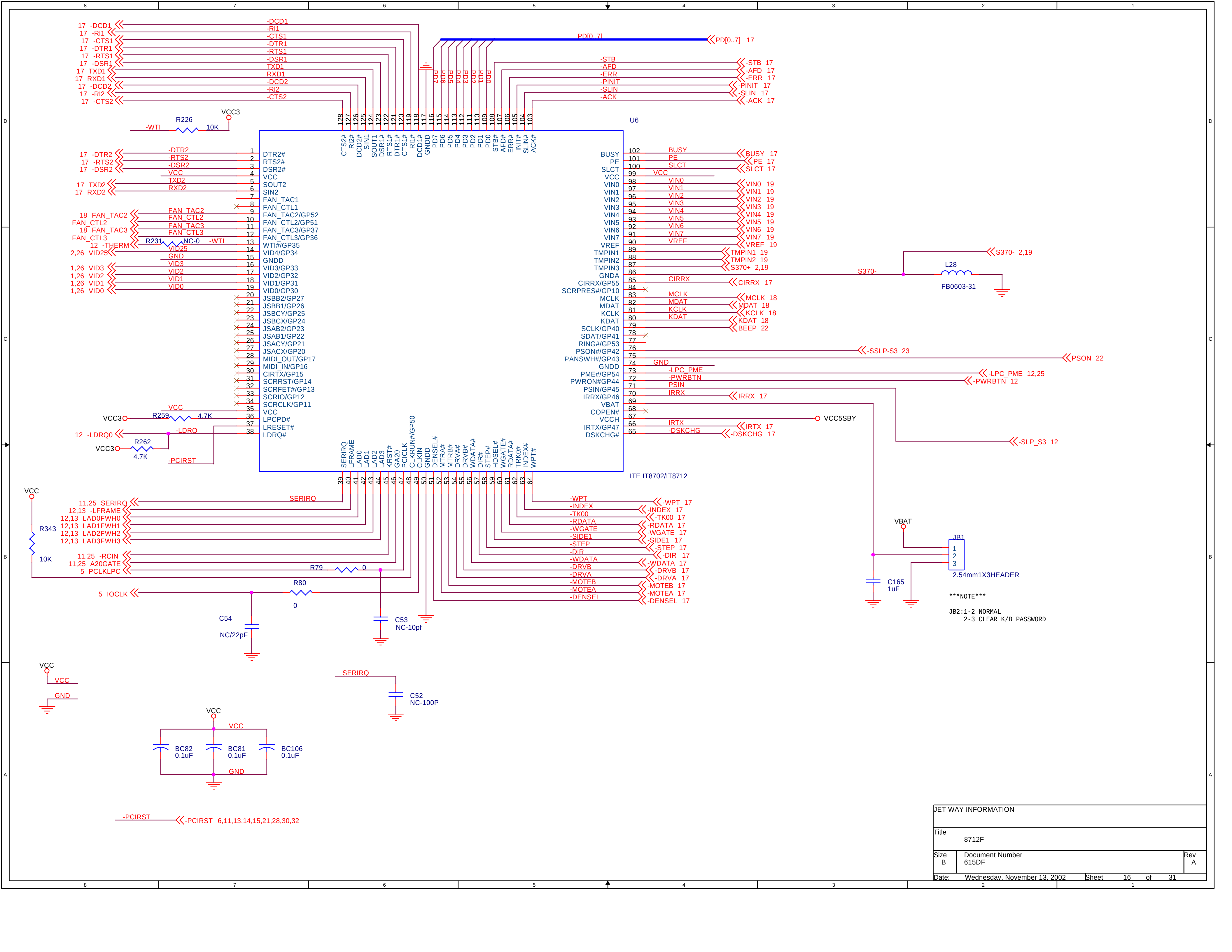
VPP and WP# are tied to VCC3
in this configuration Write Protection is register based
with the exception of the Boot Block.

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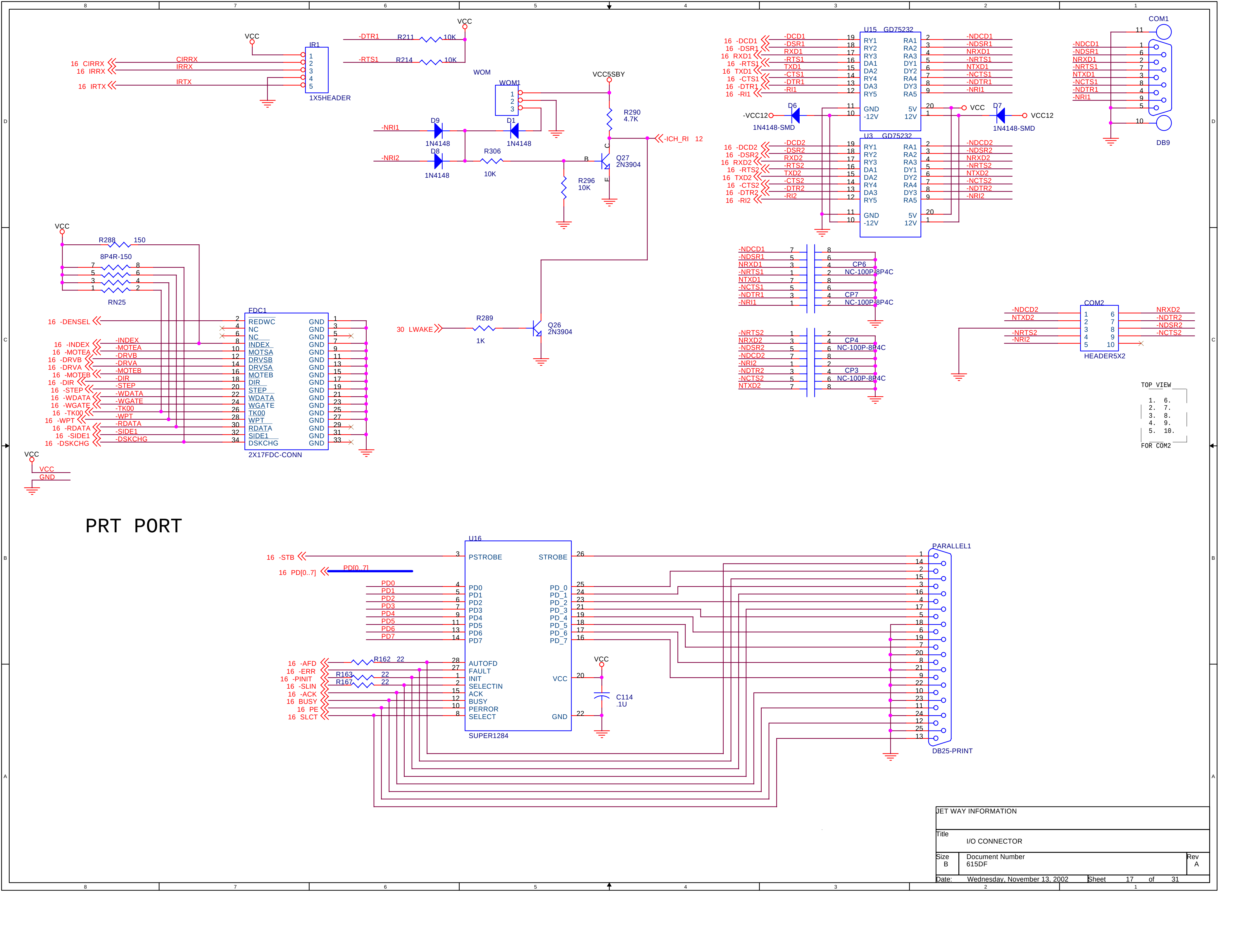


A,B,C,D
AD:25
PCI1
PCI_CON
-PGNT[4..0]
-PREQ[4..0]
VCC3SBY
RN13
2.7K-8P4R
-PIRQG 12
-PIRQF 12
-PIRQE 12
-PIRQH 12
-PTRST
R300 5.6K
PTCK
R313 5.6K
R302 0
5 PCLKPCI1
C191
NC/10pF
R303 100
AD25
11,28,30 AD[31..0]
11,28,30 -C_BE[3..0]
6,11,13,15,16,21,28,30,32 -PCIRST
BC121 0.1uF
BC119 0.1uF
BC29 0.1uF
BC120 0.1uF
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VCC
R307
4.7K
VCC
R297
4.7K
12 PDD[15..0]
PDD[15..0]
-PCIRST
R310
1K
B
C
E
Q28
MMBT3904
Q25
MMBT3904
VCC
R221
1K
-PCIRSTB
R292
33
-R_RSTP
IDE1
PDD7
PDD6
PDD5
PDD4
PDD3
PDD2
PDD1
PDD0
PDD8
PDD9
PDD10
PDD11
PDD12
PDD13
PDD14
PDD15
12 PDREQ
12 -PDIOW
12 -PDIOR
12 PIORDY
12 -PDDACK
11,25 IRQ14
PDREQ
-PDIOW
-PDIOR
PIORDY
-PDDACK
IRQ14
6,11,13,14,16,21,28,30,32 -PCIRST
-PCIRST
PDA1
PDA0
PDA2
P66DETEC 12,25
12 -PDCS1
22 -IDEACTP
-PDCS1
-IDEACTP
-PDCS3 12
HEADER20X2
C139
0.047uF
12 PDA[2..0]
PDA[2..0]
R247
5.6K
R287
10K
R218
470
12 SDD[15..0]
SDD[15..0]
VCC
R217
1K
-PCIRSTB
R291
33
-R_RSTS
IDE2
SDD7
SDD6
SDD5
SDD4
SDD3
SDD2
SDD1
SDD0
SDD8
SDD9
SDD10
SDD11
SDD12
SDD13
SDD14
SDD15
12 SDREQ
12 -SDIOW
12 -SDIOR
12 SIORDY
12 -SDDACK
11,25 IRQ15
SDREQ
-SDIOW
-SDIOR
SIORDY
-SDDACK
IRQ15
SDA1
SDA0
SDA2
S66DETEC 12,25
-SDCS3 12
12 -SDCS1
22 -IDEACTS
-SDCS1
-IDEACTS
HEADER20X2
C138
0.047uF
12 SDA[2..0]
SDA[2..0]
R246
5.6K
R286
10K
R220
470
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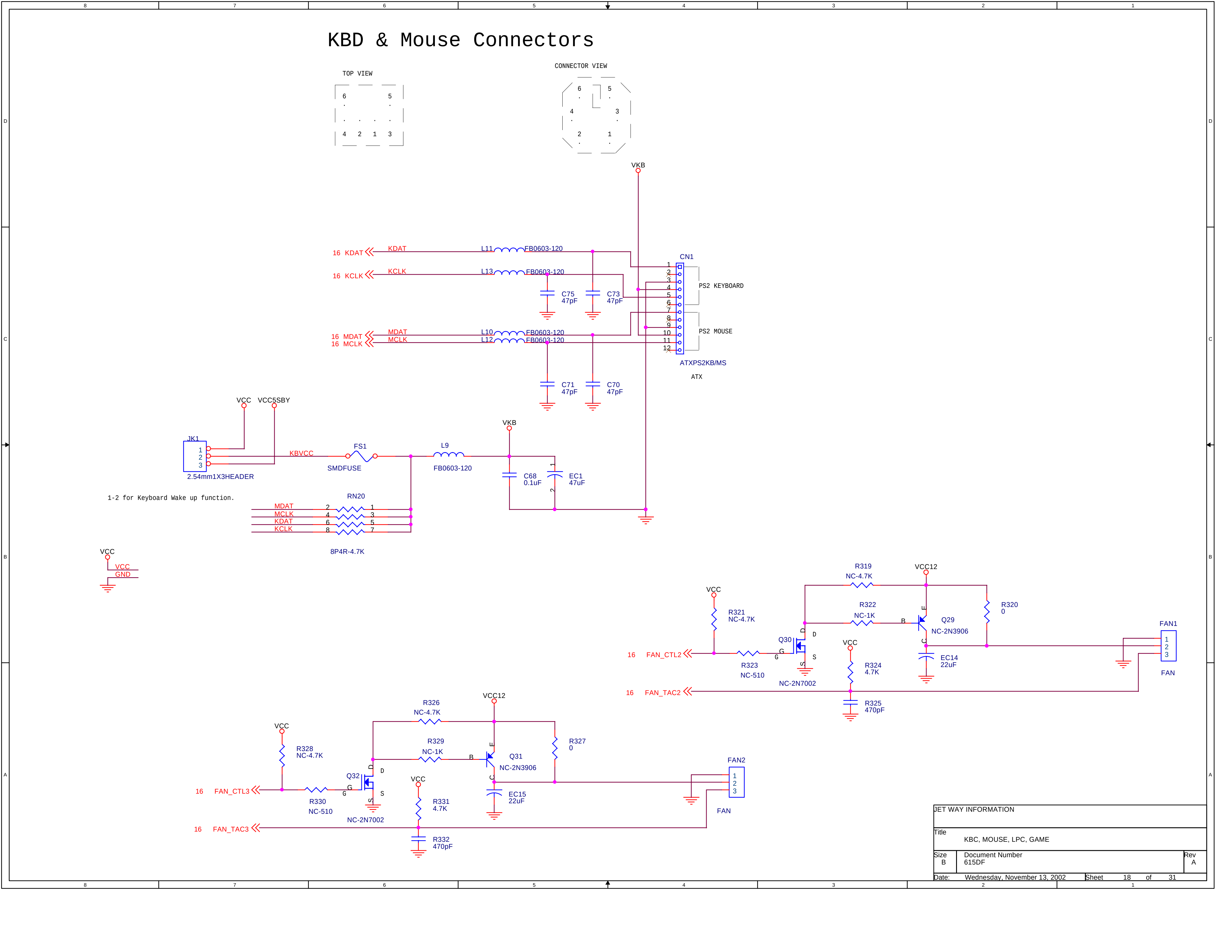


U6
ITE IT8702/IT8712
DTR2# RTS2# DSR2# VCC SOUT2 SIN2 FAN_TAC1 FAN_CTL1 FAN_TAC2/GP52 FAN_CTL2/GP51 FAN_TAC3/GP37 FAN_CTL3/GP36 WTI#/GP35 VID4/GP34 GNDD VID3/GP33 VID2/GP32 VID1/GP31 VID0/GP30 JSBB2/GP27 JSBB1/GP26 JSBCY/GP25 JSBCX/GP24 JSAB2/GP23 JSAB1/GP22 JSACY/GP21 JSACX/GP20 MIDI_OUT/GP17 MIDI_IN/GP16 CIRTX/GP15 SCRRST/GP14 SCRFET#/GP13 SCRIO/GP12 SCRCLK/GP11 VCC LPCPD# LRESET# LDRQ#
BUSY PE SLCT VCC VIN0 VIN1 VIN2 VIN3 VIN4 VIN5 VIN6 VIN7 VREF TMPIN1 TMPIN2 TMPIN3 GNDA CIRRX/GP55 SCRPRES#/GP10 MCLK MDAT KCLK KDAT SCLK/GP40 SDAT/GP41 RING#/GP53 PSON#/GP42 PANSWH#/GP43 GNDD PME#/GP54 PWRON#/GP44 PSIN/GP45 IRRX/GP46 VBAT COPEN# VCCH IRTX/GP47 DSKCHG#
CTS2# RI2# DCD2# SIN1 SOUT1 DSR1# RTS1# DTR1# CTS1# RI1# DCD1# GNDD PD7 PD6 PD5 PD4 PD3 PD2 PD1 PD0 STB# AFD# ERR# INIT# SLIN# ACK#
SERIRQ LFRAME LAD0 LAD1 LAD2 LAD3 KRST# GA20 PCICLK CLKRUN#/GP50 CLKIN GNDD DENSEL# MTRA# MTRB# DRVA# WDATA# DIR# STEP# HDSEL# WGATE# RDATA# TRK0# INDEX# WPT#
17 -DCD1, 17 -RI1, 17 -CTS1, 17 -DTR1, 17 -RTS1, 17 -DSR1, 17 TXD1, 17 RXD1, 17 -DCD2, 17 -RI2, 17 -CTS2
PD[0..7] 17
-STB 17, -AFD 17, -ERR 17, -PINIT 17, -SLIN 17, -ACK 17
R226 10K VCC3 -WTI
17 -DTR2, 17 -RTS2, 17 -DSR2, 17 TXD2, 17 RXD2
18 FAN_TAC2, FAN_CTL2, 18 FAN_TAC3, FAN_CTL3, 12 -THERM, R231 NC-0 -WTI
2,26 VID25, 1,26 VID3, 1,26 VID2, 1,26 VID1, 1,26 VID0
VCC3 R259 4.7K, 12 -LDRQ0, VCC3 R262 4.7K, -PCIRST
BUSY 17, PE 17, SLCT 17, VIN0 19, VIN1 19, VIN2 19, VIN3 19, VIN4 19, VIN5 19, VIN6 19, VIN7 19, VREF 19
TMPIN1 19, TMPIN2 19, S370+ 2,19, S370- 2,19, L28 FB0603-31
CIRRX 17, MCLK 18, MDAT 18, KCLK 18, KDAT 18, BEEP 22
-SSLP-S3 23, PSON 22, -LPC_PME 12,25, -PWRBTN 12, IRRX 17, VCC5SBY, IRTX 17, -DSKCHG 17, -SLP_S3 12
VCC R343 10K
11,25 SERIRQ, 12,13 -LFRAME, 12,13 LAD0FWH0, 12,13 LAD1FWH1, 12,13 LAD2FWH2, 12,13 LAD3FWH3, 11,25 -RCIN, 11,25 A20GATE, 5 PCLKLPC, 5 IOCLK
R79 0, R80 0, C54 NC/22pF, C53 NC-10pf
-WPT 17, -INDEX 17, -TK00 17, -RDATA 17, -WGATE 17, -SIDE1 17, -STEP 17, -DIR 17, -WDATA 17, -DRVB 17, -DRVA 17, -MOTEB 17, -MOTEA 17, -DENSEL 17
VBAT, JB1, 2.54mm1X3HEADER, C165 1uF
NOTE
JB2:1-2 NORMAL
2-3 CLEAR K/B PASSWORD
SERIRQ C52 NC-100P
VCC BC82 0.1uF, BC81 0.1uF, BC106 0.1uF, GND
-PCIRST 6,11,13,14,15,21,28,30,32
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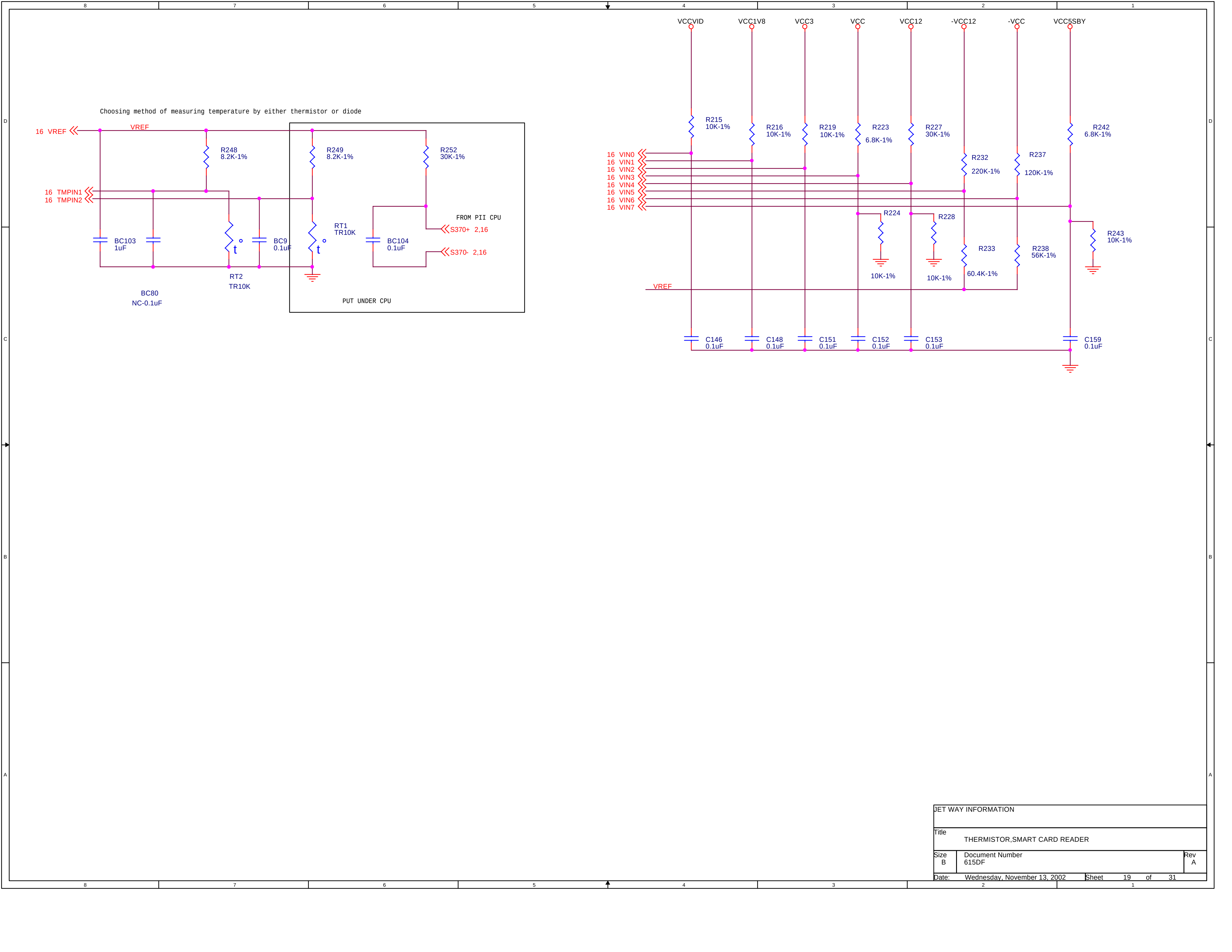


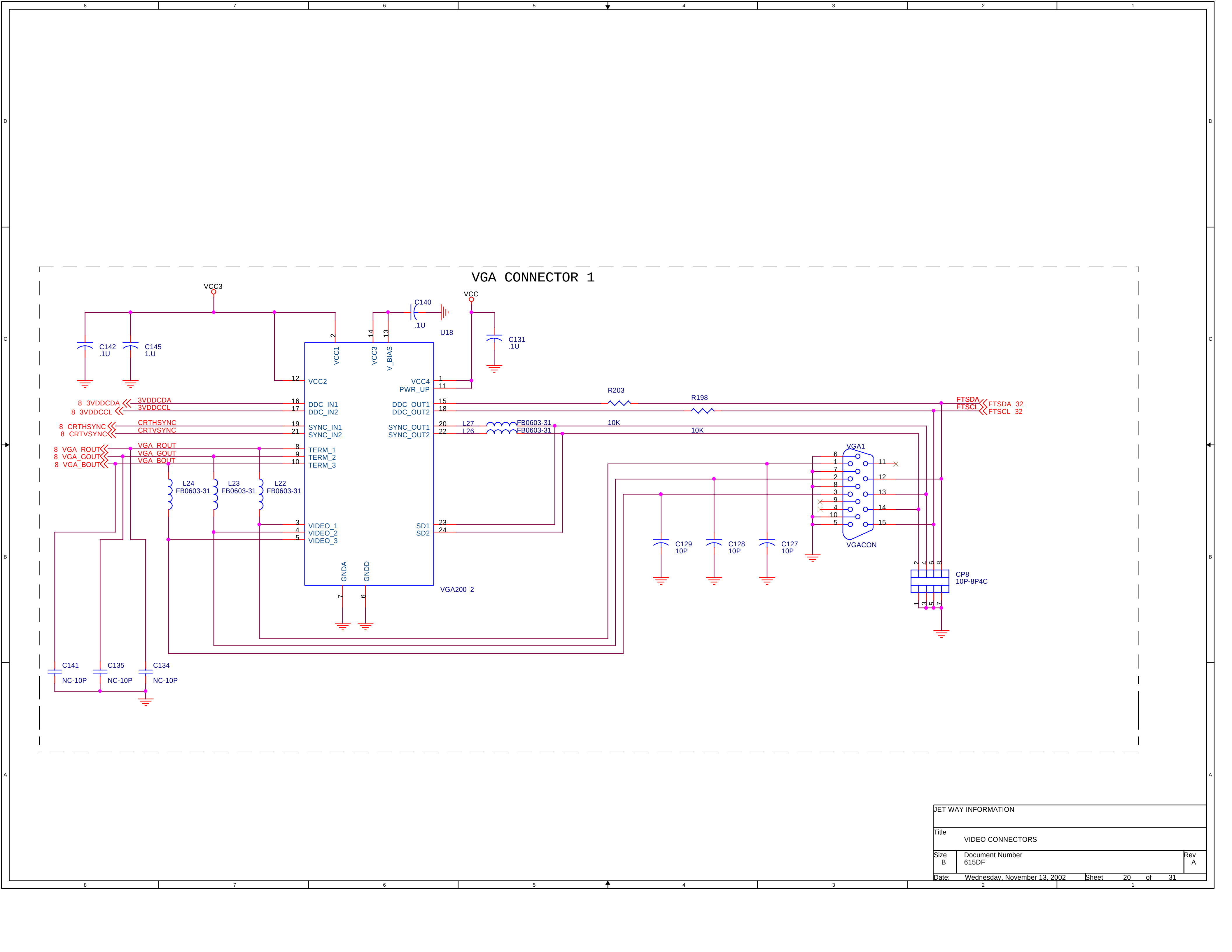
PRT PORT
IR1
1X5HEADER
16 CIRRX
16 IRRX
16 IRTX
CIRRX
IRRX
IRTX
VCC
-DTR1
R211
10K
-RTS1
R214
10K
WOM
WOM1
VCC5SBY
R290
4.7K
-NRI1
D9
1N4148
D1
1N4148
-NRI2
D8
1N4148
R306
10K
R296
10K
Q27
2N3904
-ICH_RI 12
U15 GD75232
U3 GD75232
RY1
RY2
RY3
DA1
DA2
RY4
DA3
RY5
RA1
RA2
RA3
DY1
DY2
RA4
DY3
RA5
GND
-12V
5V
12V
-VCC12
D6
1N4148-SMD
D7
1N4148-SMD
VCC12
16 -DCD1
16 -DSR1
16 RXD1
16 -RTS1
16 TXD1
16 -CTS1
16 -DTR1
16 -RI1
16 -DCD2
16 -DSR2
16 RXD2
16 -RTS2
16 TXD2
16 -CTS2
16 -DTR2
16 -RI2
-NDCD1
-NDSR1
NRXD1
-NRTS1
NTXD1
-NCTS1
-NDTR1
-NRI1
-NDCD2
-NDSR2
NRXD2
-NRTS2
NTXD2
-NCTS2
-NDTR2
-NRI2
COM1
DB9
CP6
NC-100P-8P4C
CP7
NC-100P-8P4C
CP4
NC-100P-8P4C
CP3
NC-100P-8P4C
COM2
HEADER5X2
TOP VIEW
1. 6.
2. 7.
3. 8.
4. 9.
5. 10.
FOR COM2
R288 150
8P4R-150
RN25
FDC1
2X17FDC-CONN
16 -DENSEL
16 -INDEX
16 -MOTEA
16 -DRVB
16 -DRVA
16 -MOTEB
16 -DIR
16 -STEP
16 -WDATA
16 -WGATE
16 -TK00
16 -WPT
16 -RDATA
16 -SIDE1
16 -DSKCHG
REDWC
NC
INDEX
MOTSA
DRVSB
DRVSA
MOTEB
DIR
STEP
WDATA
WGATE
TK00
WPT
RDATA
SIDE1
DSKCHG
30 LWAKE
R289
1K
Q26
2N3904
U16
SUPER1284
16 -STB
16 PD[0..7]
PD[0..7]
PD0
PD1
PD2
PD3
PD4
PD5
PD6
PD7
PSTROBE
STROBE
PD_0
PD_1
PD_2
PD_3
PD_4
PD_5
PD_6
PD_7
16 -AFD
16 -ERR
16 -PINIT
16 -SLIN
16 -ACK
16 BUSY
16 PE
16 SLCT
R162 22
R163 22
R167 22
AUTOFD
FAULT
INIT
SELECTIN
ACK
BUSY
PERROR
SELECT
VCC
GND
C114
.1U
PARALLEL1
DB25-PRINT
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KBD & Mouse Connectors

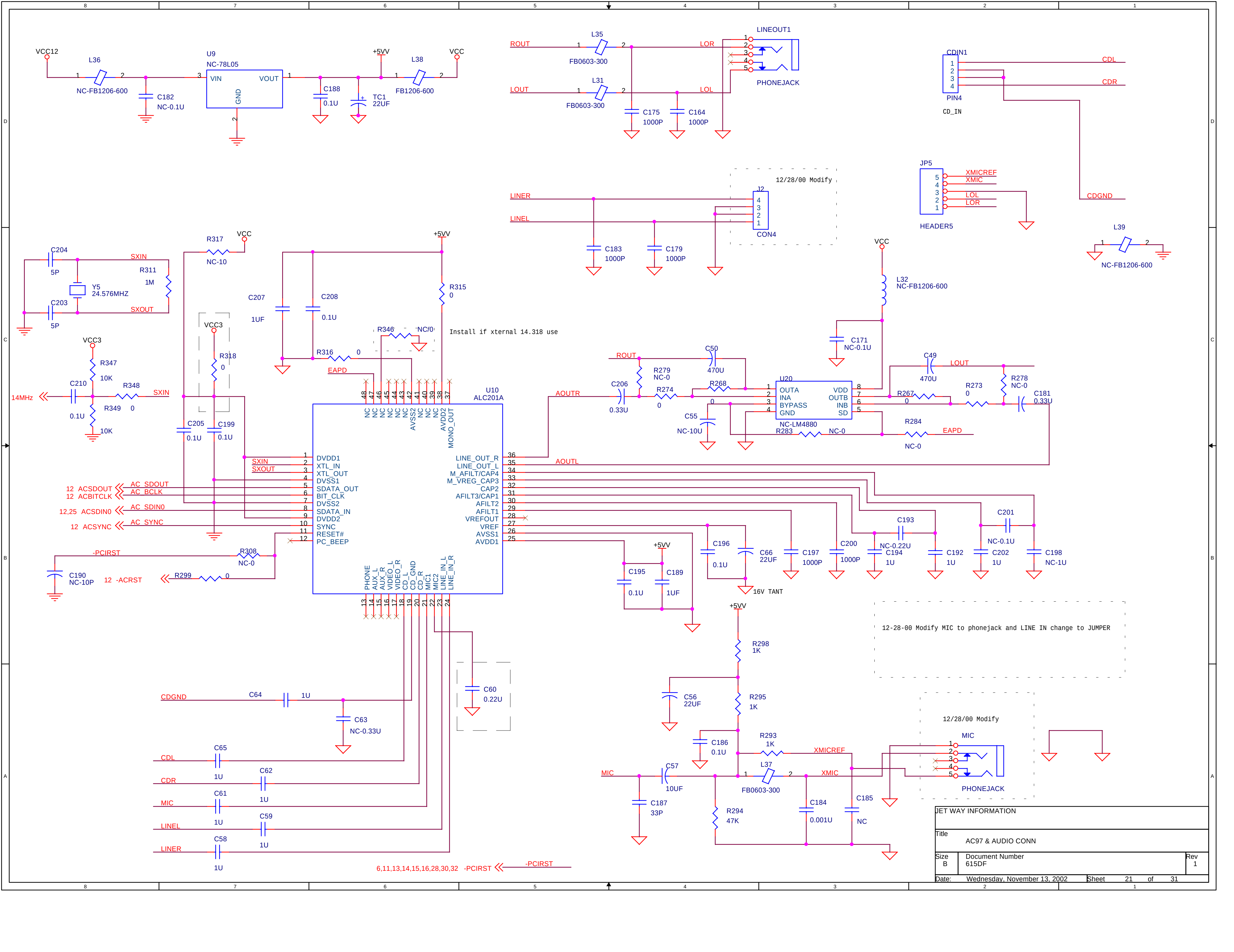


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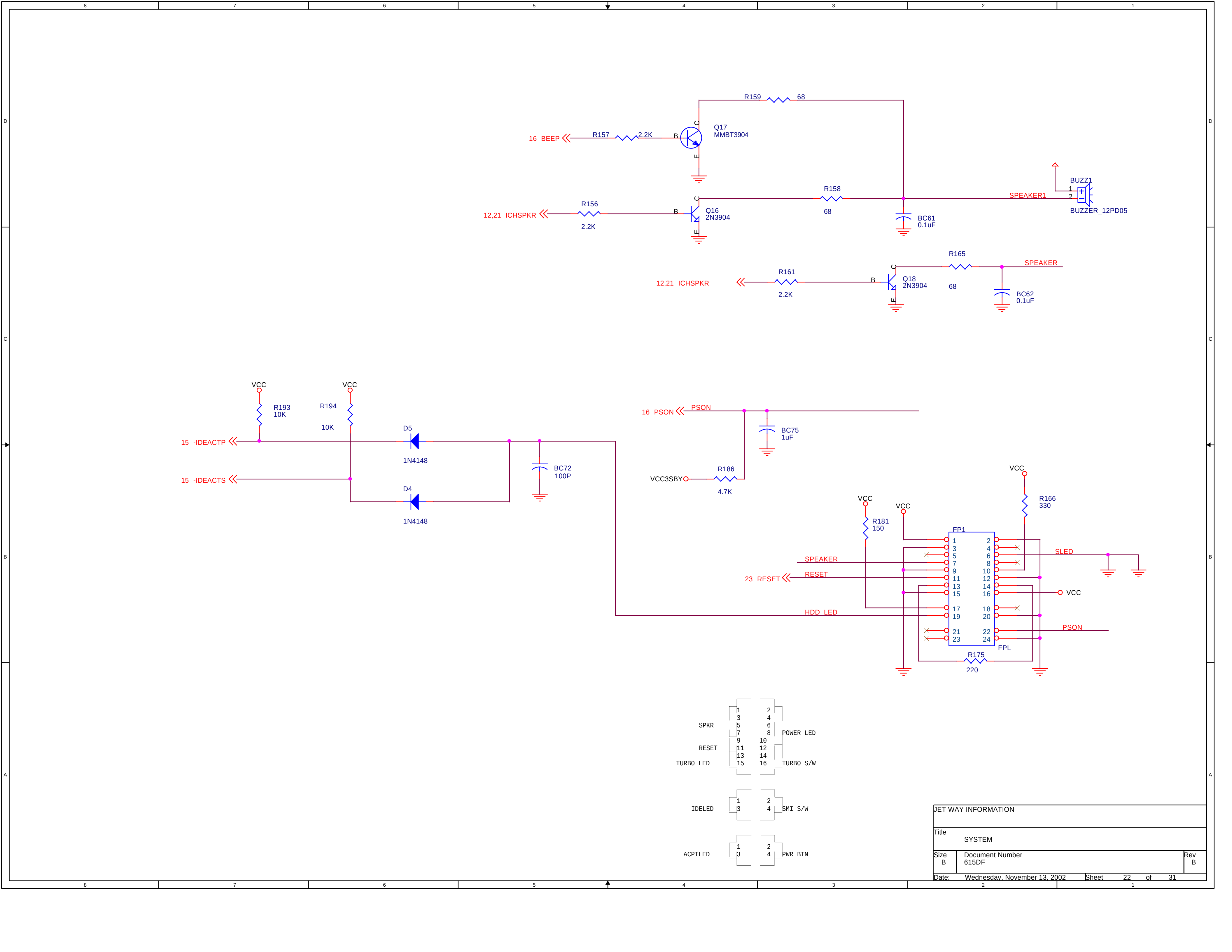

Choosing method of measuring temperature by either thermistor or diode
16 VREF
VREF
R248
8.2K-1%
R249
8.2K-1%
R252
30K-1%
16 TMPIN1
16 TMPIN2
BC103
1uF
BC9
0.1uF
RT1
TR10K
BC104
0.1uF
FROM PII CPU
S370+ 2,16
S370- 2,16
RT2
TR10K
BC80
NC-0.1uF
PUT UNDER CPU
VCCVID
VCC1V8
VCC3
VCC
VCC12
-VCC12
-VCC
VCC5SBY
R215
10K-1%
R216
10K-1%
R219
10K-1%
R223
6.8K-1%
R227
30K-1%
R242
6.8K-1%
R232
220K-1%
R237
120K-1%
16 VIN0
16 VIN1
16 VIN2
16 VIN3
16 VIN4
16 VIN5
16 VIN6
16 VIN7
R224
10K-1%
R228
10K-1%
R243
10K-1%
R233
60.4K-1%
R238
56K-1%
VREF
C146
0.1uF
C148
0.1uF
C151
0.1uF
C152
0.1uF
C153
0.1uF
C159
0.1uF
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THERMISTOR,SMART CARD READER
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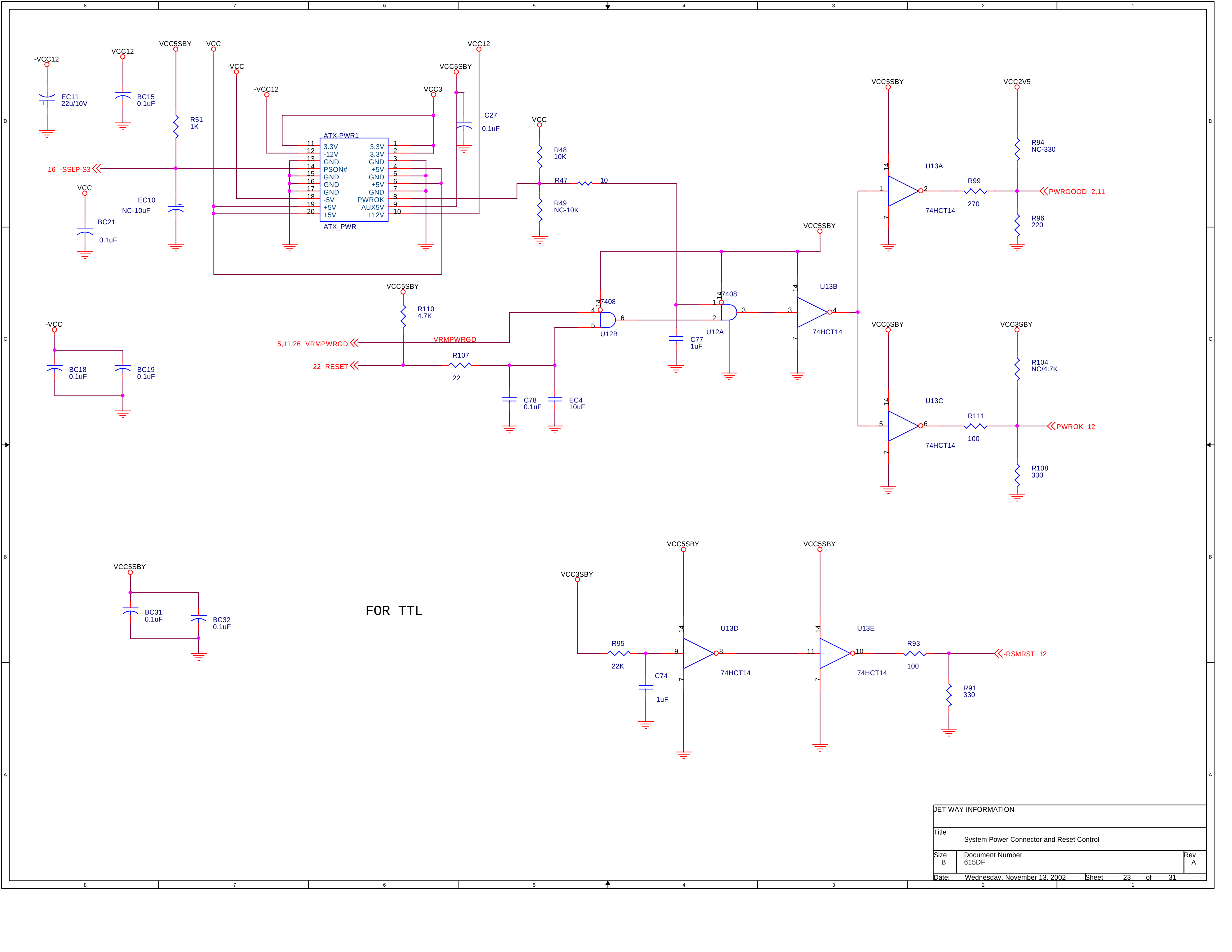
VGA CONNECTOR 1
VCC3
VCC
C140
.1U
U18
C142
.1U
C145
1.U
C131
.1U
VCC1
VCC3
V_BIAS
VCC2
VCC4
PWR_UP
DDC_IN1
DDC_IN2
DDC_OUT1
DDC_OUT2
SYNC_IN1
SYNC_IN2
SYNC_OUT1
SYNC_OUT2
TERM_1
TERM_2
TERM_3
VIDEO_1
VIDEO_2
VIDEO_3
SD1
SD2
GNDA
GNDD
VGA200_2
8 3VDDCDA
8 3VDDCCL
3VDDCDA
3VDDCCL
8 CRTHSYNC
8 CRTVSYNC
CRTHSYNC
CRTVSYNC
8 VGA_ROUT
8 VGA_GOUT
8 VGA_BOUT
VGA_ROUT
VGA_GOUT
VGA_BOUT
L24
FB0603-31
L23
FB0603-31
L22
FB0603-31
L27
L26
FB0603-31
FB0603-31
R203
10K
R198
10K
FTSDA
FTSCL
FTSDA 32
FTSCL 32
VGA1
VGACON
C129
10P
C128
10P
C127
10P
CP8
10P-8P4C
C141
NC-10P
C135
NC-10P
C134
NC-10P
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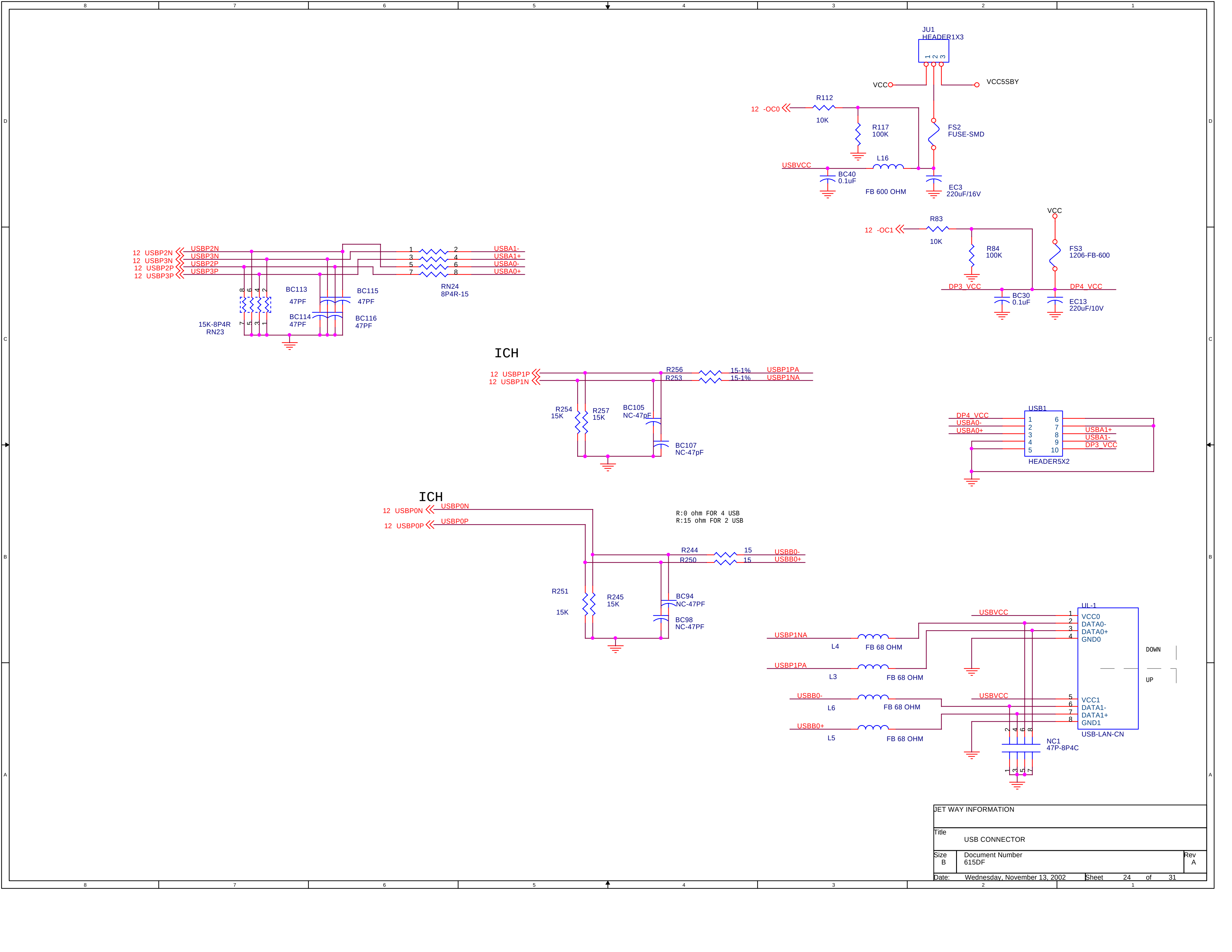
U9
NC-78L05
VIN
VOUT
GND
VCC12
L36
NC-FB1206-600
C182
NC-0.1U
C188
0.1U
TC1
22UF
+5VV
L38
FB1206-600
VCC
ROUT
L35
FB0603-300
LOR
LINEOUT1
PHONEJACK
LOUT
L31
FB0603-300
LOL
C175
1000P
C164
1000P
CDIN1
PIN4
CD_IN
CDL
CDR
CDGND
12/28/00 Modify
J2
CON4
LINER
LINEL
C183
1000P
C179
1000P
JP5
HEADER5
XMICREF
XMIC
LOL
LOR
L39
NC-FB1206-600
C204
5P
C203
5P
SXIN
SXOUT
R311
1M
Y5
24.576MHZ
R317
NC-10
VCC
+5VV
C207
1UF
C208
0.1U
R315
0
R346
NC/0
Install if xternal 14.318 use
VCC3
R318
0
R316
0
EAPD
VCC3
R347
10K
C210
R348
SXIN
14MHz
0.1U
R349
0
10K
C205
0.1U
C199
0.1U
U10
ALC201A
DVDD1
XTL_IN
XTL_OUT
DVSS1
SDATA_OUT
BIT_CLK
DVSS2
SDATA_IN
DVDD2
SYNC
RESET#
PC_BEEP
LINE_OUT_R
LINE_OUT_L
M_AFILT/CAP4
M_VREG_CAP3
CAP2
AFILT3/CAP1
AFILT2
AFILT1
VREFOUT
VREF
AVSS1
AVDD1
AVSS2
AVDD2
MONO_OUT
PHONE
AUX_L
AUX_R
VIDEO_L
VIDEO_R
CD_L
CD_GND
CD_R
MIC1
MIC2
LINE_IN_L
LINE_IN_R
12 ACSDOUT
AC_SDOUT
12 ACBITCLK
AC_BCLK
12,25 ACSDIN0
AC_SDIN0
12 ACSYNC
AC_SYNC
-PCIRST
R308
NC-0
C190
NC-10P
12 -ACRST
R299
0
AOUTR
AOUTL
ROUT
R279
NC-0
C50
470U
C206
0.33U
R274
0
R268
C55
NC-10U
U20
NC-LM4880
OUTA
INA
BYPASS
GND
VDD
OUTB
INB
SD
R283
NC-0
R284
NC-0
EAPD
C171
NC-0.1U
VCC
L32
NC-FB1206-600
C49
470U
LOUT
R267
0
R273
0
R278
NC-0
C181
0.33U
C196
0.1U
C66
22UF
16V TANT
C197
1000P
C200
1000P
C193
NC-0.22U
C194
1U
C192
1U
C201
NC-0.1U
C202
1U
C198
NC-1U
+5VV
C195
0.1U
C189
1UF
12-28-00 Modify MIC to phonejack and LINE IN change to JUMPER
C60
0.22U
CDGND
C64
1U
C63
NC-0.33U
CDL
C65
1U
CDR
C62
1U
MIC
C61
1U
LINEL
C59
1U
LINER
C58
1U
6,11,13,14,15,16,28,30,32 -PCIRST
-PCIRST
+5VV
R298
1K
C56
22UF
R295
1K
C186
0.1U
R293
1K
XMICREF
MIC
C57
10UF
L37
FB0603-300
XMIC
C187
33P
R294
47K
C184
0.001U
C185
NC
12/28/00 Modify
MIC
PHONEJACK
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ATX-PWR1
ATX_PWR
3.3V
-12V
GND
PSON#
-5V
+5V
3.3V
+5V
PWROK
AUX5V
+12V
-VCC12
VCC12
VCC5SBY
VCC
-VCC
VCC3
VCC2V5
VCC3SBY
EC11 22u/10V
BC15 0.1uF
R51 1K
16 -SSLP-S3
EC10 NC-10uF
BC21 0.1uF
C27 0.1uF
R48 10K
R47 10
R49 NC-10K
U13A
74HCT14
R99 270
R94 NC-330
R96 220
PWRGOOD 2,11
U13B
U12A
U12B
7408
C77 1uF
R110 4.7K
5,11,26 VRMPWRGD
VRMPWRGD
22 RESET
R107 22
C78 0.1uF
EC4 10uF
BC18 0.1uF
BC19 0.1uF
U13C
R111 100
R104 NC/4.7K
R108 330
PWROK 12
BC31 0.1uF
BC32 0.1uF
FOR TTL
R95 22K
C74 1uF
U13D
U13E
R93 100
R91 330
-RSMRST 12
JET WAY INFORMATION
Title
System Power Connector and Reset Control
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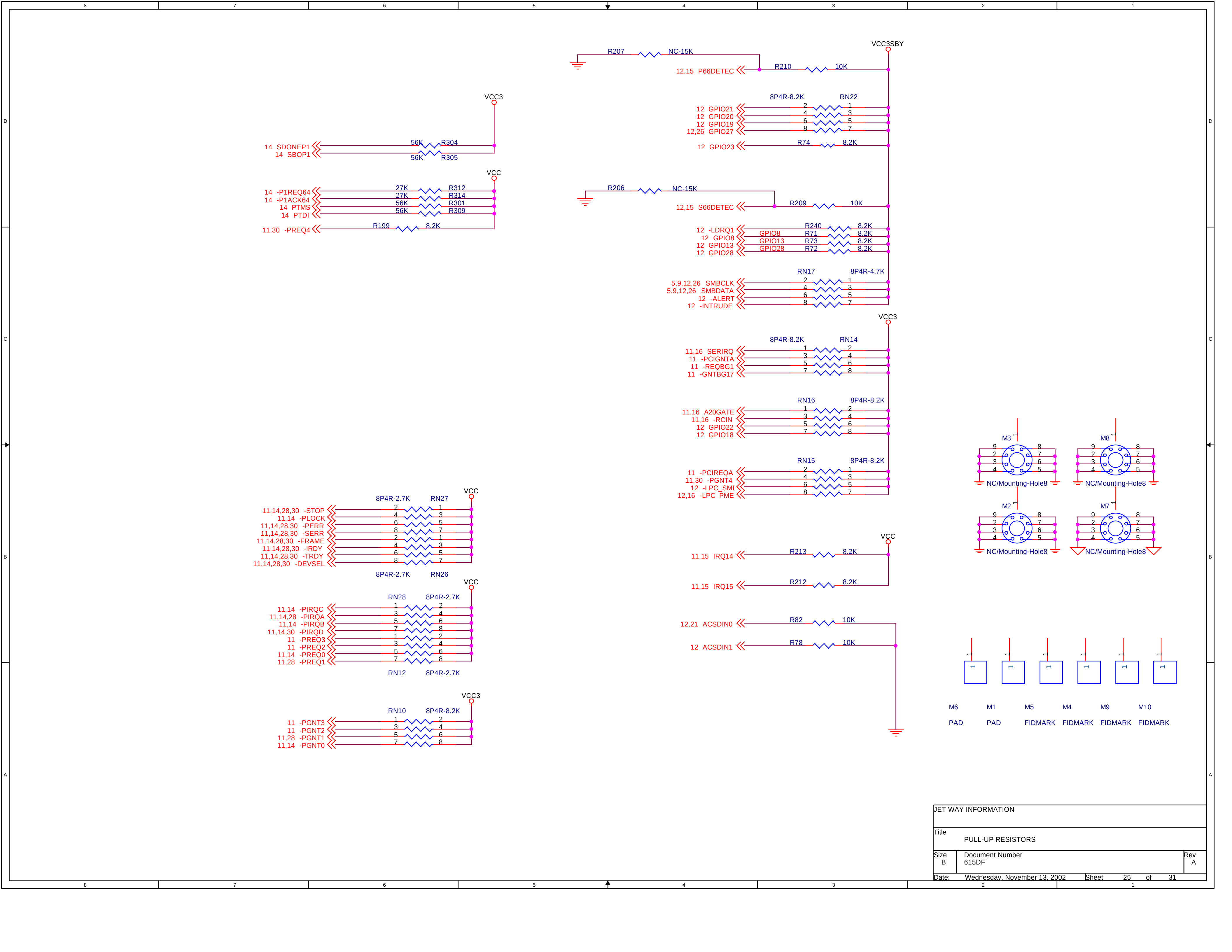


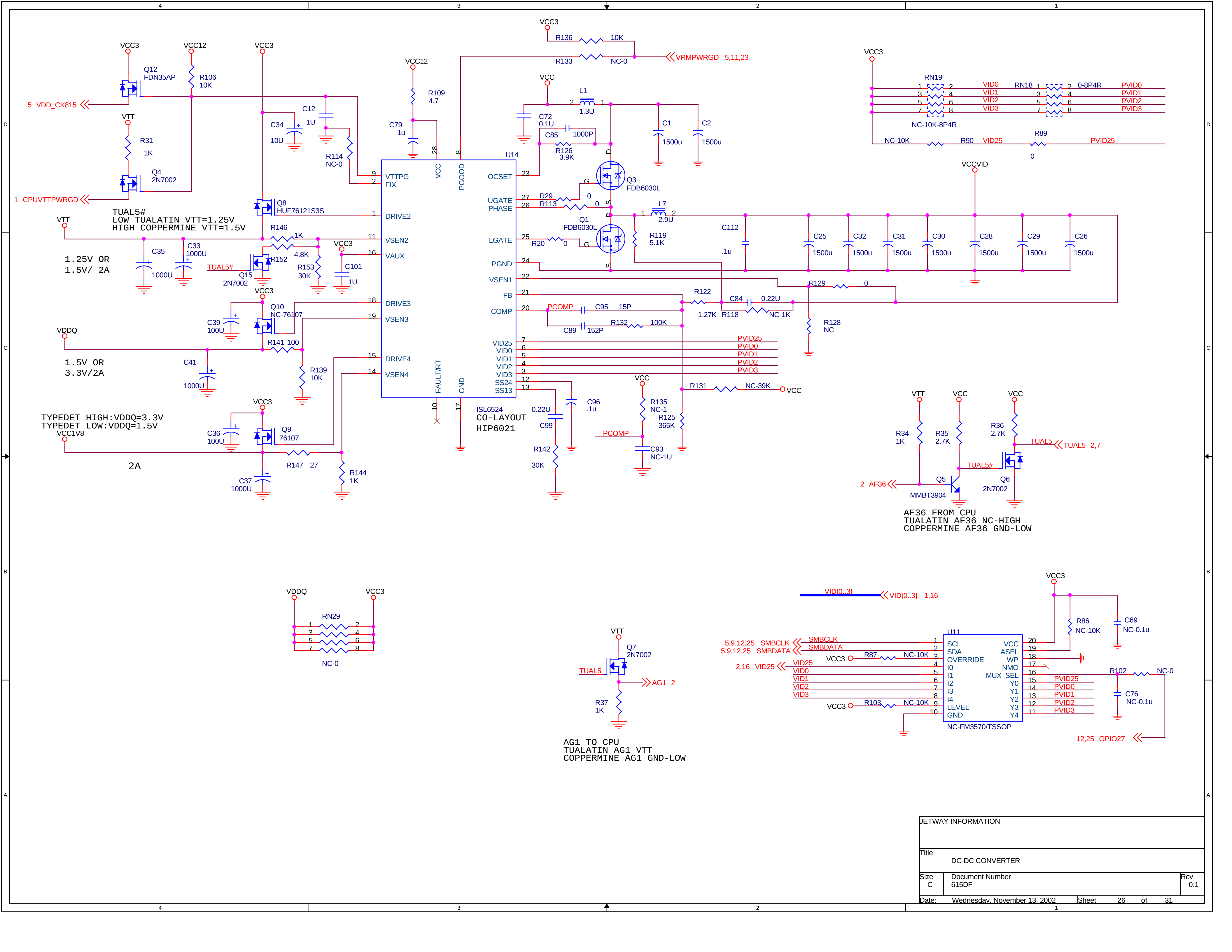
JET WAY INFORMATION

Title: USB CONNECTOR

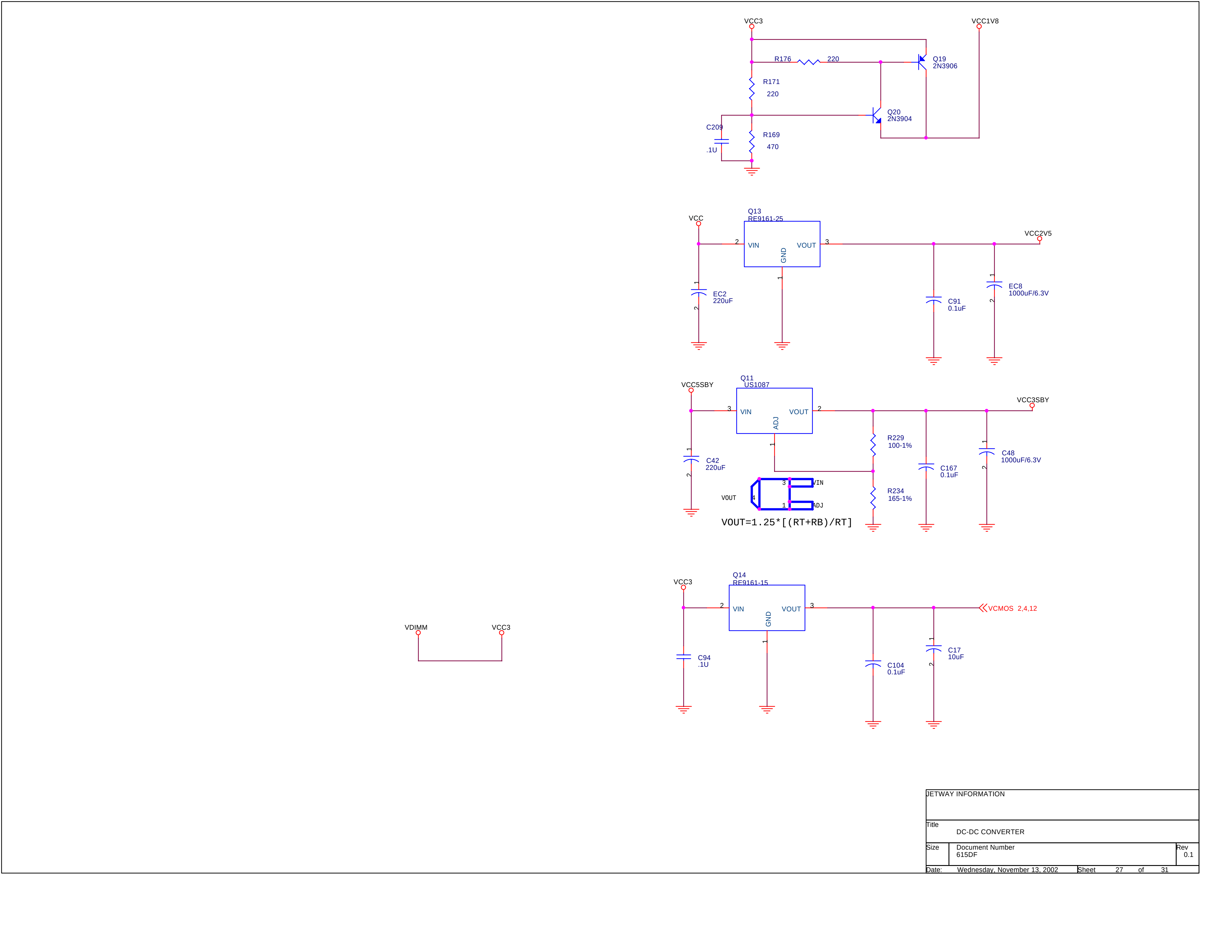
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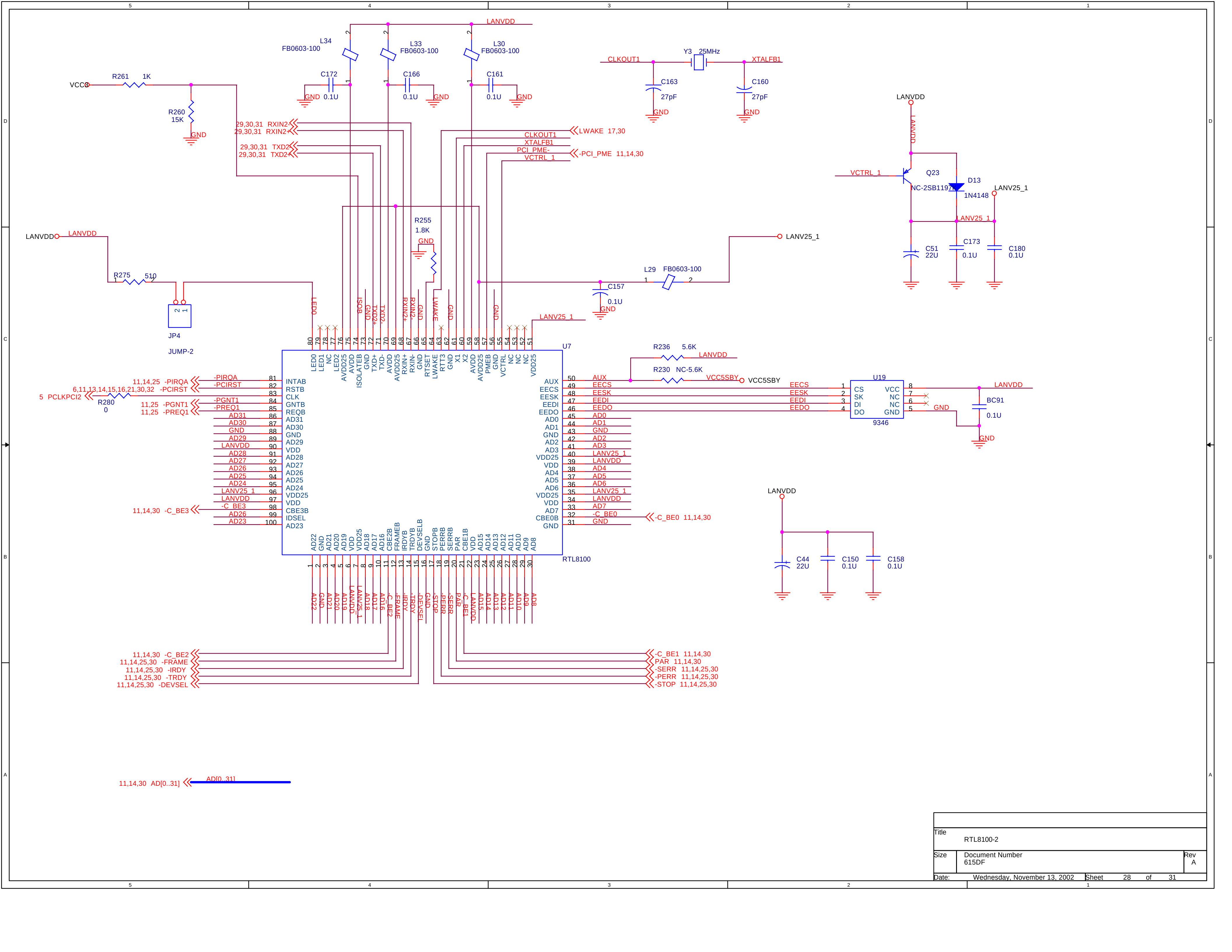
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VCC3SBY
R207 NC-15K
R210 10K
12,15 P66DETEC
8P4R-8.2K RN22
12 GPIO21
12 GPIO20
12 GPIO19
12,26 GPIO27
R74 8.2K
12 GPIO23
R206 NC-15K
R209 10K
12,15 S66DETEC
R240 8.2K
R71 8.2K
R73 8.2K
R72 8.2K
12 -LDRQ1
12 GPIO8
12 GPIO13
12 GPIO28
RN17 8P4R-4.7K
5,9,12,26 SMBCLK
5,9,12,26 SMBDATA
12 -ALERT
12 -INTRUDE
VCC3
8P4R-8.2K RN14
11,16 SERIRQ
11 -PCIGNTA
11 -REQBG1
11 -GNTBG17
RN16 8P4R-8.2K
11,16 A20GATE
11,16 -RCIN
12 GPIO22
12 GPIO18
RN15 8P4R-8.2K
11 -PCIREQA
11,30 -PGNT4
12 -LPC_SMI
12,16 -LPC_PME
VCC
R213 8.2K
11,15 IRQ14
R212 8.2K
11,15 IRQ15
R82 10K
12,21 ACSDIN0
R78 10K
12 ACSDIN1
VCC3
56K R304
56K R305
14 SDONEP1
14 SBOP1
VCC
27K R312
27K R314
56K R301
56K R309
14 -P1REQ64
14 -P1ACK64
14 PTMS
14 PTDI
R199 8.2K
11,30 -PREQ4
VCC
8P4R-2.7K RN27
11,14,28,30 -STOP
11,14 -PLOCK
11,14,28,30 -PERR
11,14,28,30 -SERR
11,14,28,30 -FRAME
11,14,28,30 -IRDY
11,14,28,30 -TRDY
11,14,28,30 -DEVSEL
8P4R-2.7K RN26
VCC
RN28 8P4R-2.7K
11,14 -PIRQC
11,14,28 -PIRQA
11,14 -PIRQB
11,14,30 -PIRQD
11 -PREQ3
11 -PREQ2
11,14 -PREQ0
11,28 -PREQ1
RN12 8P4R-2.7K
VCC3
RN10 8P4R-8.2K
11 -PGNT3
11 -PGNT2
11,28 -PGNT1
11,14 -PGNT0
M3 NC/Mounting-Hole8
M8 NC/Mounting-Hole8
M2 NC/Mounting-Hole8
M7 NC/Mounting-Hole8
M6 PAD
M1 PAD
M5 FIDMARK
M4 FIDMARK
M9 FIDMARK
M10 FIDMARK
JET WAY INFORMATION
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PULL-UP RESISTORS
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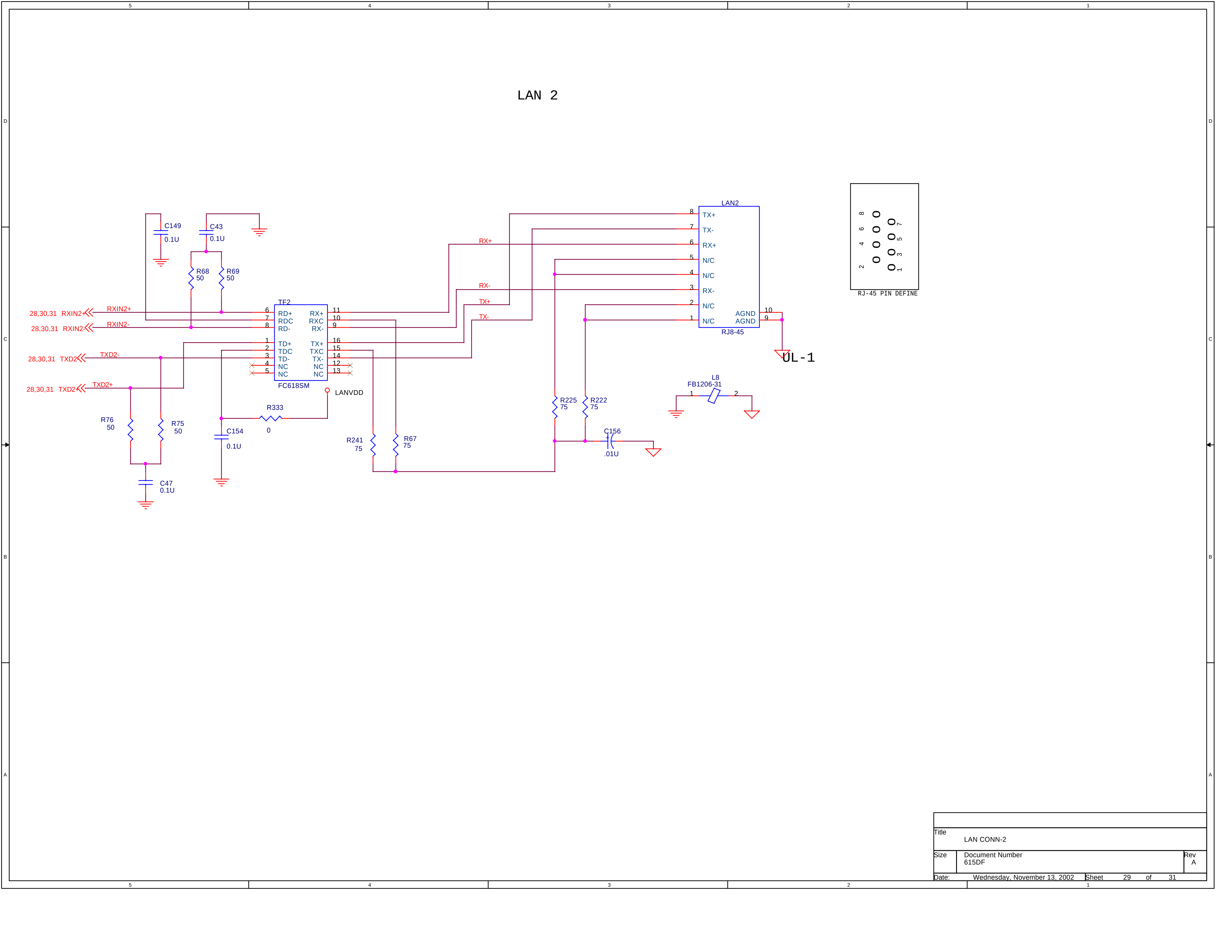


TUAL5#
LOW TUALATIN VTT=1.25V
HIGH COPPERMINE VTT=1.5V
1.25V OR
1.5V/ 2A
1.5V OR
3.3V/2A
TYPEDET HIGH:VDDQ=3.3V
TYPEDET LOW:VDDQ=1.5V
2A
ISL6524
CO-LAYOUT
HIP6021
AF36 FROM CPU
TUALATIN AF36 NC-HIGH
COPPERMINE AF36 GND-LOW
AG1 TO CPU
TUALATIN AG1 VTT
COPPERMINE AG1 GND-LOW
5 VDD_CK815
1 CPUVTTPWRGD
VRMPWRGD 5,11,23
TUAL5 2,7
2 AF36
VID[0..3] 1,16
5,9,12,25 SMBCLK
5,9,12,25 SMBDATA
2,16 VID25
AG1 2
12,25 GPIO27
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VCC3
VCC1V8
R176
220
Q19
2N3906
R171
220
Q20
2N3904
C209
.1U
R169
470
VCC
Q13
RF9161-25
VIN
VOUT
GND
VCC2V5
EC2
220uF
C91
0.1uF
EC8
1000uF/6.3V
VCC5SBY
Q11
US1087
VIN
VOUT
ADJ
VCC3SBY
R229
100-1%
C42
220uF
C167
0.1uF
C48
1000uF/6.3V
R234
165-1%
VOUT
VIN
ADJ
VOUT=1.25*[(RT+RB)/RT]
VCC3
Q14
RF9161-15
VIN
VOUT
GND
VCMOS 2,4,12
VDIMM
VCC3
C94
.1U
C104
0.1uF
C17
10uF
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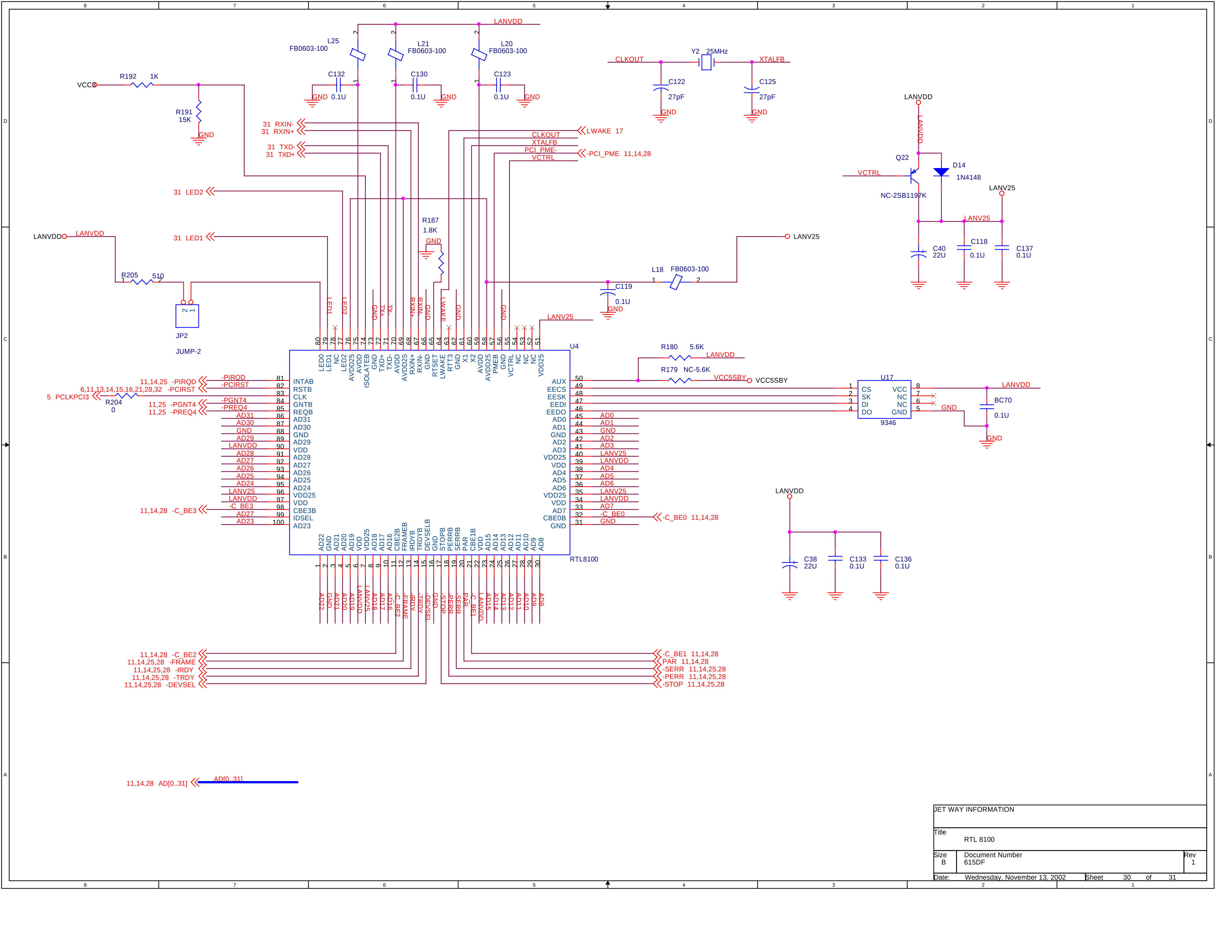
LANVDD
L34
FB0603-100
L33
FB0603-100
L30
FB0603-100
C172
0.1U
C166
0.1U
C161
0.1U
GND
R261 1K
VCC3
R260
15K
CLKOUT1
Y3 25MHz
XTALFB1
C163
27pF
C160
27pF
29,30,31 RXIN2-
29,30,31 RXIN2+
29,30,31 TXD2-
29,30,31 TXD2+
CLKOUT1
XTALFB1
PCI_PME-
VCTRL_1
LWAKE 17,30
-PCI_PME 11,14,30
VCTRL_1
Q23
NC-2SB1197K
D13
1N4148
LANV25_1
C51
22U
C173
0.1U
C180
0.1U
R255
1.8K
LANVDD
R275 510
JP4
JUMP-2
L29 FB0603-100
C157
0.1U
U7
RTL8100
R236 5.6K
R230 NC-5.6K
VCC5SBY
U19
9346
CS SK DI DO VCC NC NC GND
EECS EESK EEDI EEDO
BC91
0.1U
11,14,25 -PIRQA
6,11,13,14,15,16,21,30,32 -PCIRST
5 PCLKPCI2
R280
0
11,25 -PGNT1
11,25 -PREQ1
11,14,30 -C_BE3
-C_BE0 11,14,30
INTAB RSTB CLK GNTB REQB AD31 AD30 GND AD29 VDD AD28 AD27 AD26 AD25 AD24 VDD25 VDD CBE3B IDSEL AD23
AUX EECS EESK EEDI EEDO AD0 AD1 GND AD2 AD3 VDD25 VDD AD4 AD5 AD6 VDD25 VDD AD7 CBE0B GND
LED0 LED1 NC LED2 AVDD25 AVDD ISOLATEB GND TXD+ TXD- AVDD AVDD25 RXIN+ RXIN- GND RTSET LWAKE RTT3 GND X1 X2 AVDD AVDD25 PMEB GND VCTRL NC NC NC VDD25
AD22 GND AD21 AD20 AD19 VDD VDD25 AD18 AD17 AD16 CBE2B FRAMEB IRDYB TRDYB DEVSELB GND STOPB PERRB SERRB PAR CBE1B VDD AD15 AD14 AD13 AD12 AD11 AD10 AD9 AD8
LANVDD
C44
22U
C150
0.1U
C158
0.1U
11,14,30 -C_BE2
11,14,25,30 -FRAME
11,14,25,30 -IRDY
11,14,25,30 -TRDY
11,14,25,30 -DEVSEL
-C_BE1 11,14,30
PAR 11,14,30
-SERR 11,14,25,30
-PERR 11,14,25,30
-STOP 11,14,25,30
11,14,30 AD[0..31]
AD[0..31]
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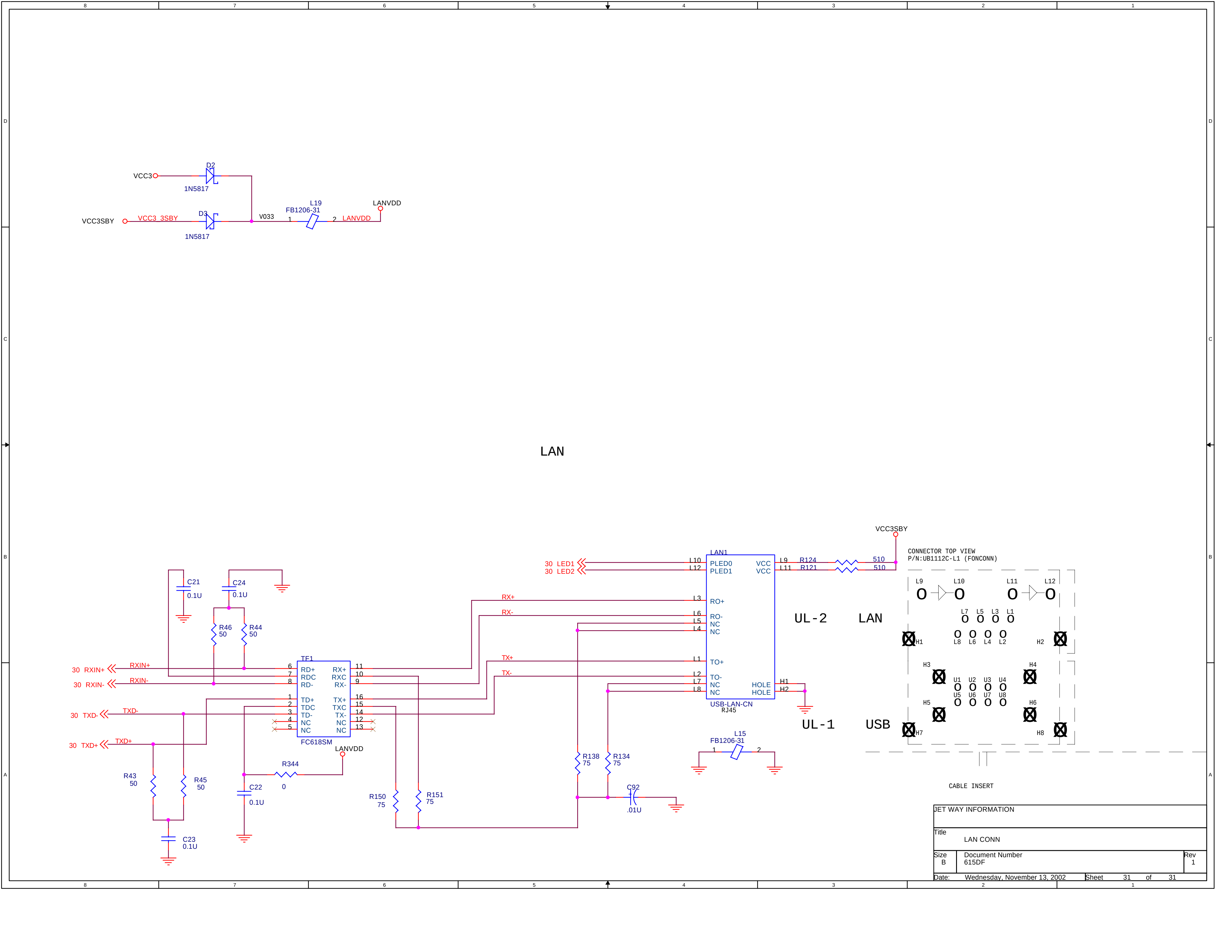
Title
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LANVDD
L25
FB0603-100
L21
FB0603-100
L20
FB0603-100
C132
0.1U
C130
0.1U
C123
0.1U
GND
CLKOUT
Y2 25MHz
XTALFB
C122
27pF
C125
27pF
VCC3
R192 1K
R191
15K
31 RXIN-
31 RXIN+
31 TXD-
31 TXD+
CLKOUT
XTALFB
PCI_PME-
VCTRL
LWAKE 17
-PCI_PME 11,14,28
Q22
VCTRL
D14
1N4148
NC-2SB1197K
LANV25
C40
22U
C118
0.1U
C137
0.1U
31 LED2
31 LED1
R187
1.8K
LANVDD
R205 510
JP2
JUMP-2
L18 FB0603-100
C119
0.1U
U4
R180 5.6K
R179 NC-5.6K
VCC5SBY
U17
9346
CS SK DI DO VCC NC NC GND
BC70
0.1U
11,14,25 -PIRQD
6,11,13,14,15,16,21,28,32 -PCIRST
5 PCLKPCI3
R204
0
11,25 -PGNT4
11,25 -PREQ4
11,14,28 -C_BE3
-C_BE0 11,14,28
INTAB RSTB CLK GNTB REQB AD31 AD30 GND AD29 VDD AD28 AD27 AD26 AD25 AD24 VDD25 VDD CBE3B IDSEL AD23
AUX EECS EESK EEDI EEDO AD0 AD1 GND AD2 AD3 VDD25 VDD AD4 AD5 AD6 VDD25 VDD AD7 CBE0B GND
RTL8100
C38
22U
C133
0.1U
C136
0.1U
11,14,28 -C_BE2
11,14,25,28 -FRAME
11,14,25,28 -IRDY
11,14,25,28 -TRDY
11,14,25,28 -DEVSEL
-C_BE1 11,14,28
PAR 11,14,28
-SERR 11,14,25,28
-PERR 11,14,25,28
-STOP 11,14,25,28
11,14,28 AD[0..31]
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